

8 7 6 5 4 3 2 1
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JAMESTOWN
KERALA

2012.10.25

21-OCT-2002		
DATE	CHANGE NO.	REV

	EE	DATE	POWER	DATE	INVENTEC			
DRAWER					TITLE MODEL,PROJECT,FUNCTION			
DESIGN								
CHECK								
RESPONSIBLE								
SIZE				VER:	SIZE	CODE	DOC NUMBER	REV
FILE NAME					C	CS	1310xxxx-0-0	X01
PIN		xxx			SHEET		1	of 06

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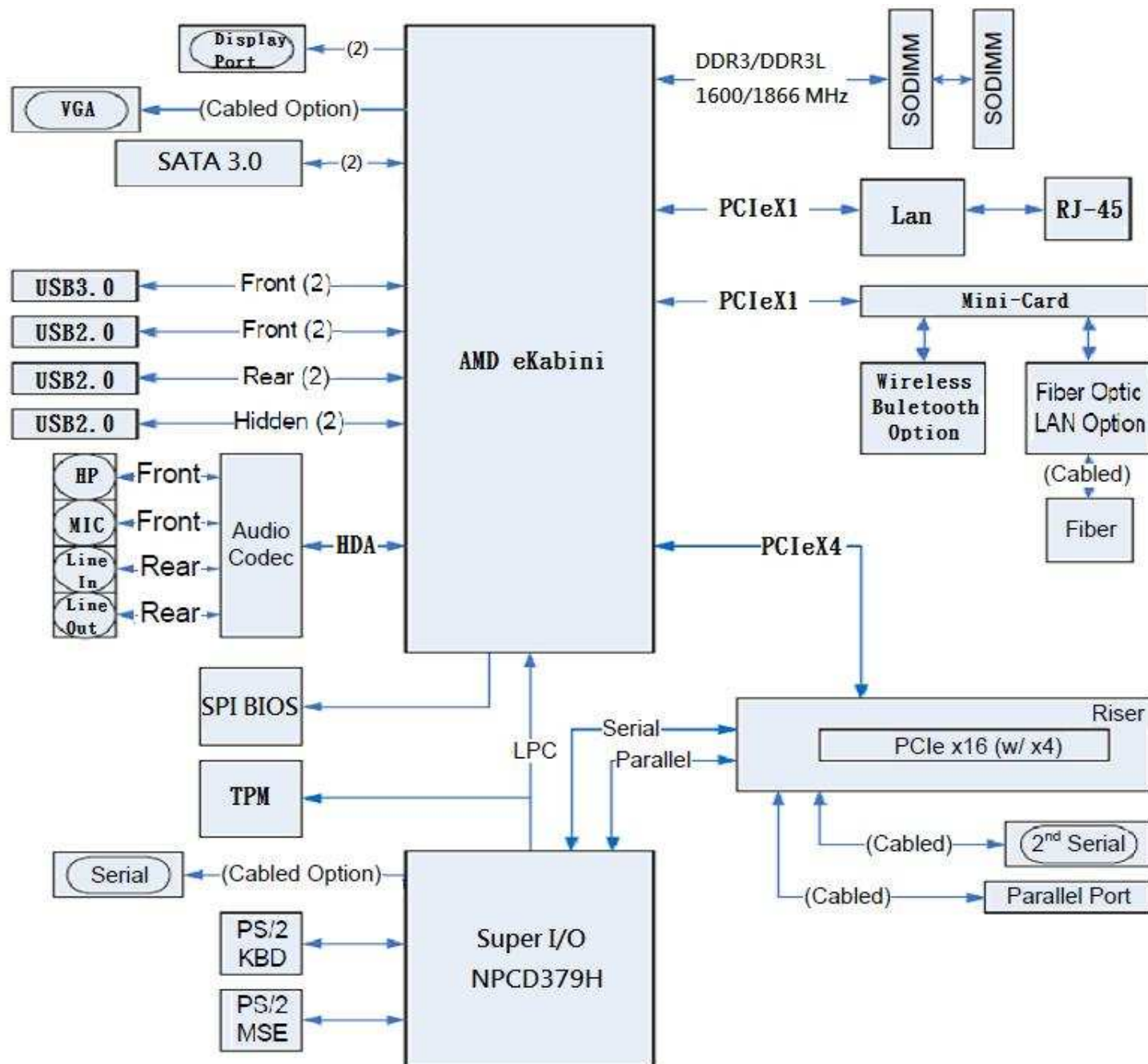
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INVENTEC

TITLE
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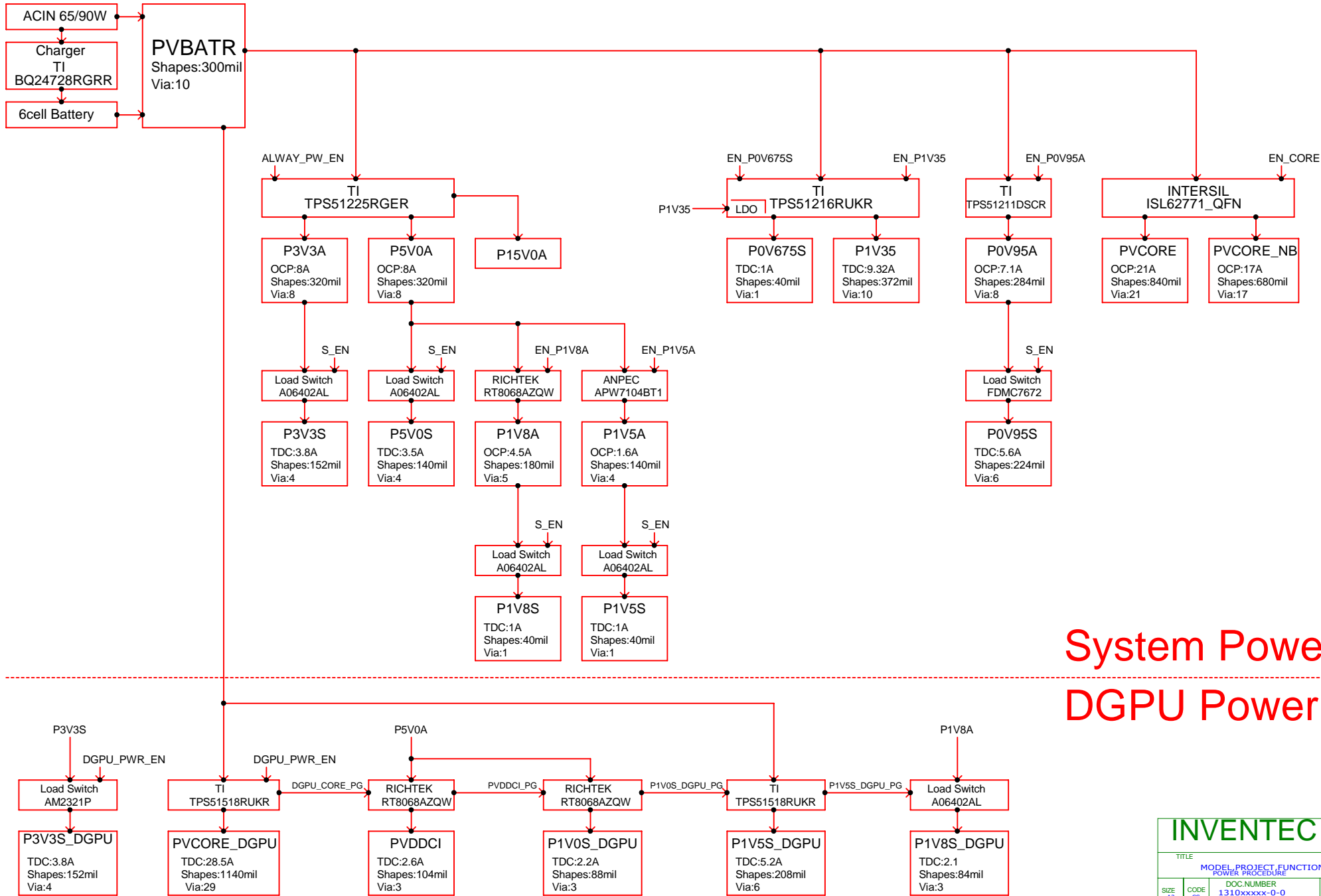
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Block Diagram
DOC NUMBER
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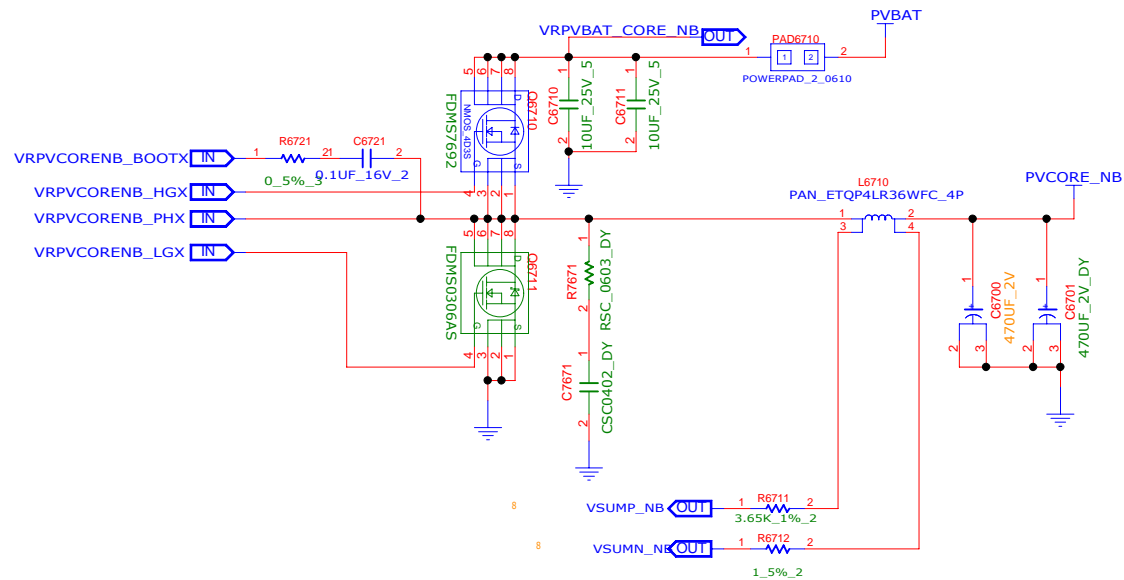


System Power

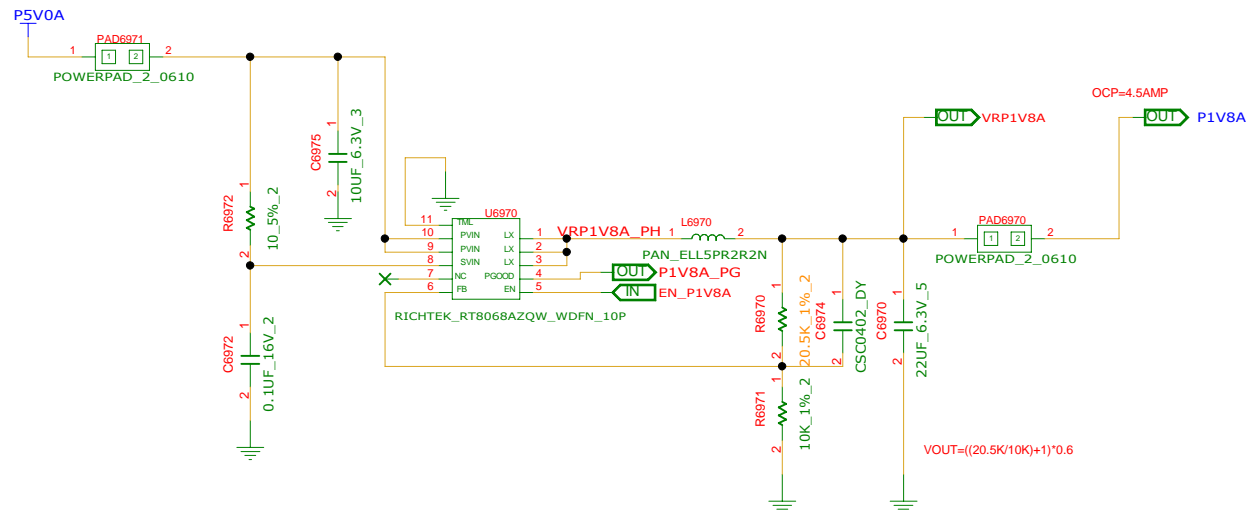
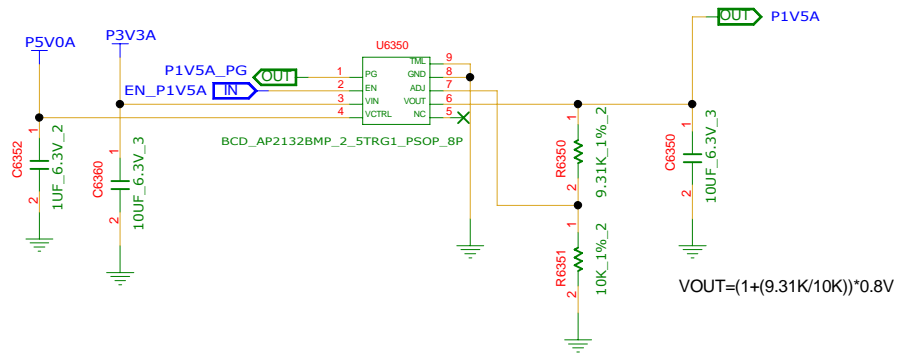
DGPU Power

INVENTEC

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MODEL PROJECT FUNCTION			
POWER PROCEDURE			
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MODEL PROJECT,FUNCTION			
VCORE_NB			
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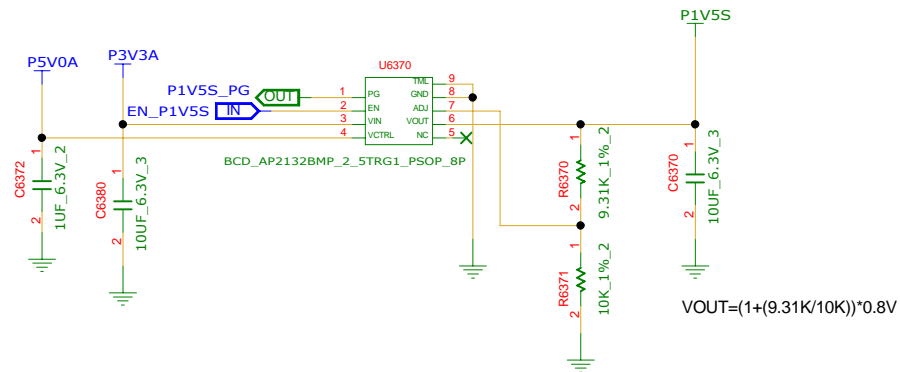
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TITLE
MODEL PROJECT FUNCTION
POWER(SLEEP: +V5S/+V3S/+V1.1S)

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A3 CS 1310xxxxx-0-0 X01

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MODEL,PROJECT,FUNCTION

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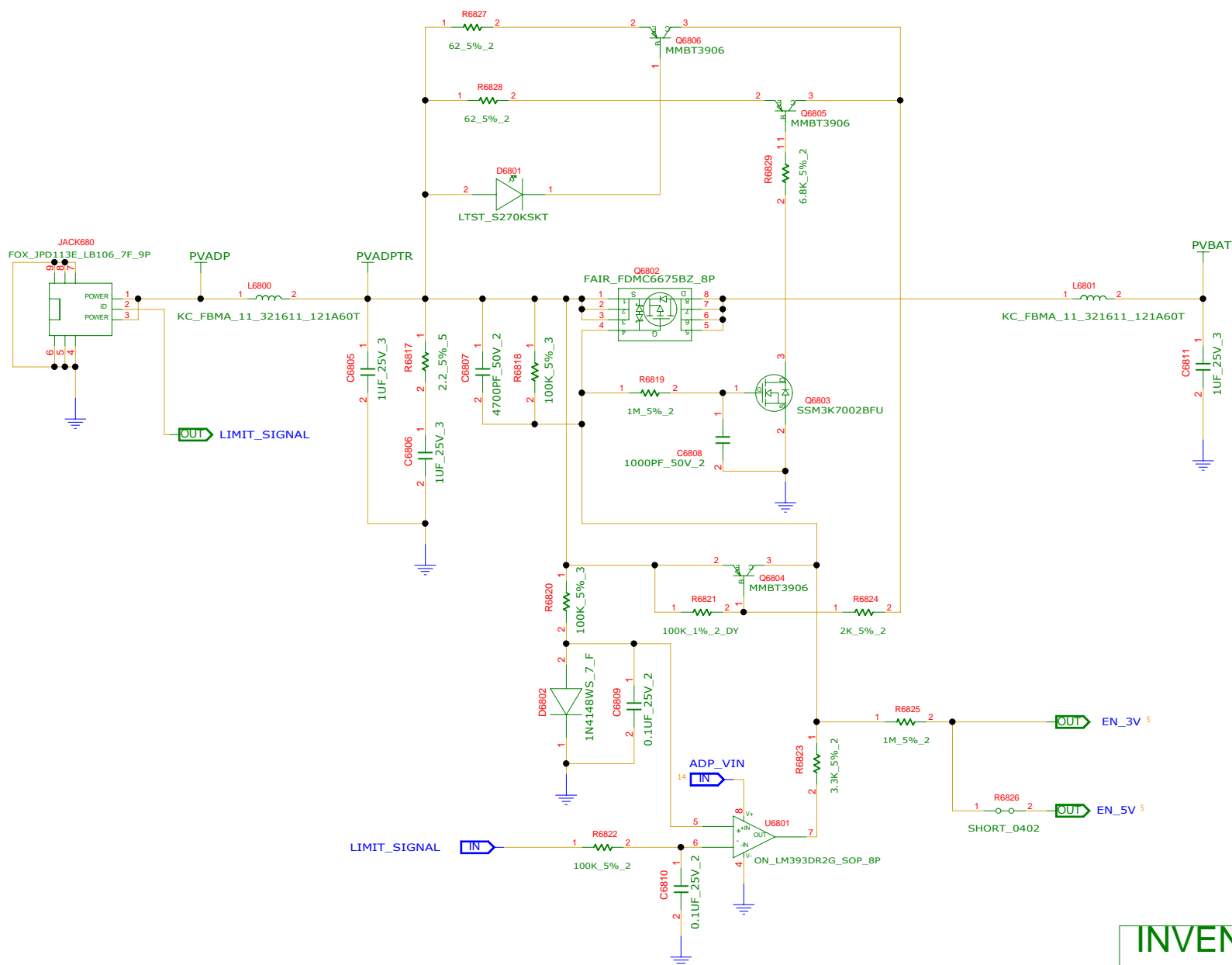
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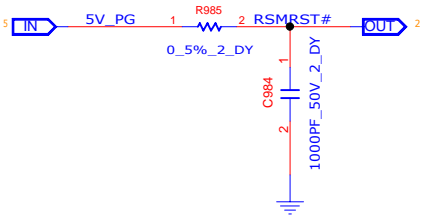
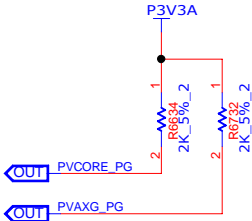
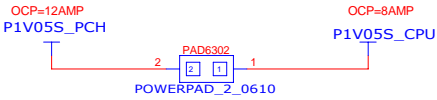
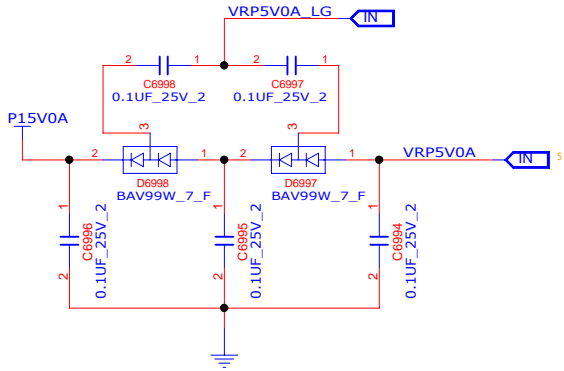
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BLANK

INVENTEC

TITLE			
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Block Diagram			
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EMI Part



INVENTEC

TITLE MODEL,PROJECT,FUNCTION

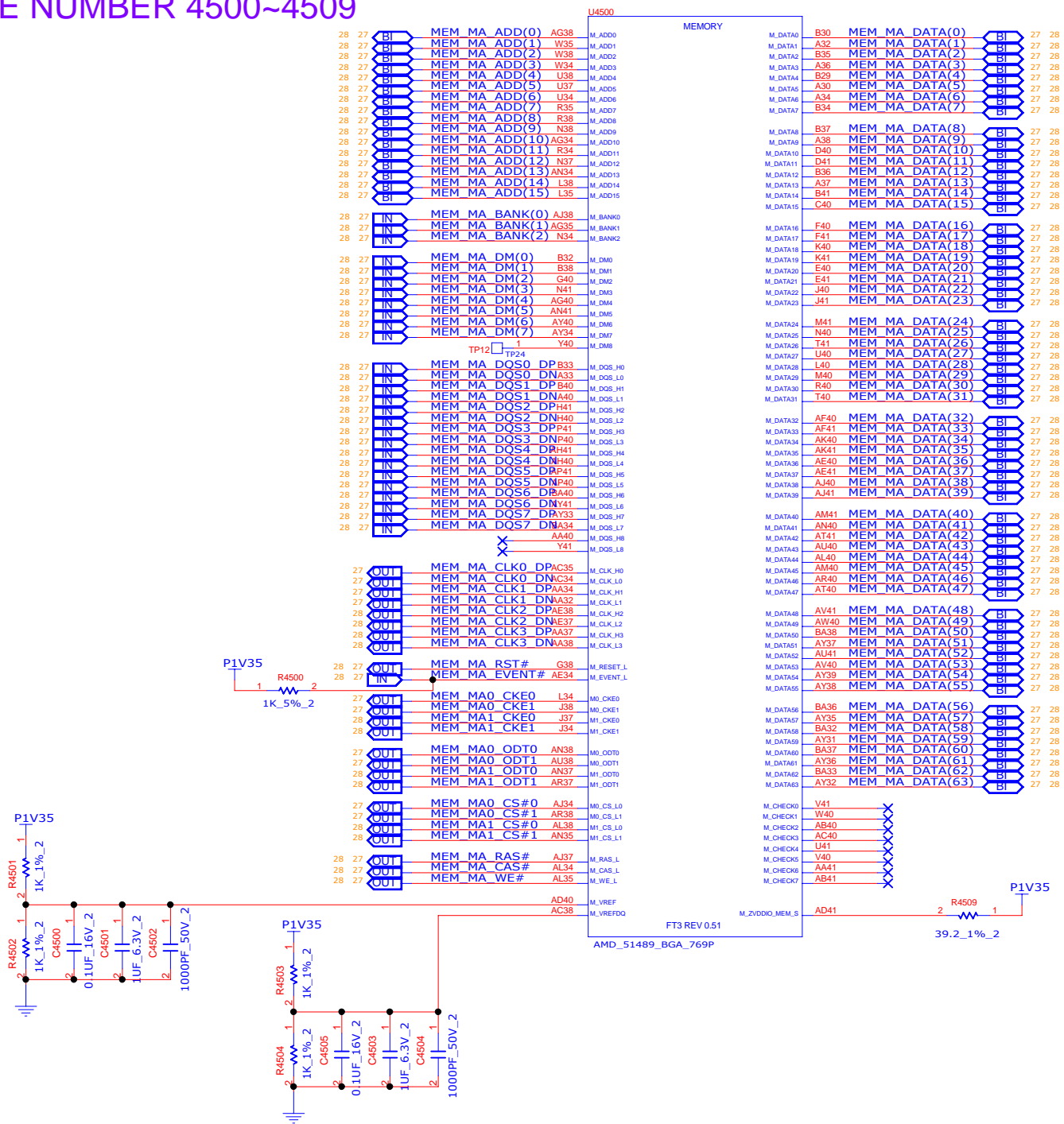
POWER TO EE PORT & EMI PART

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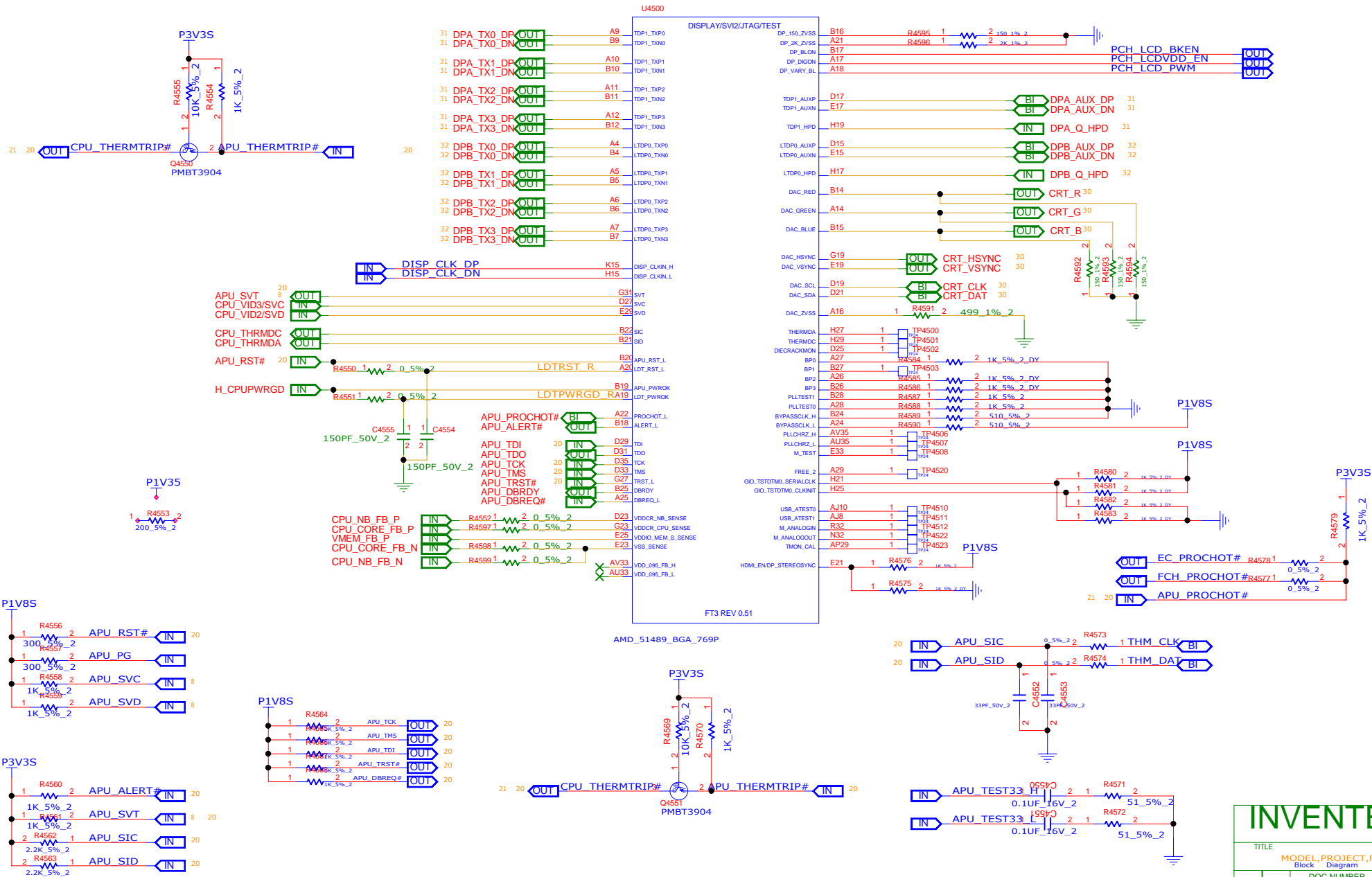
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INVENTEC

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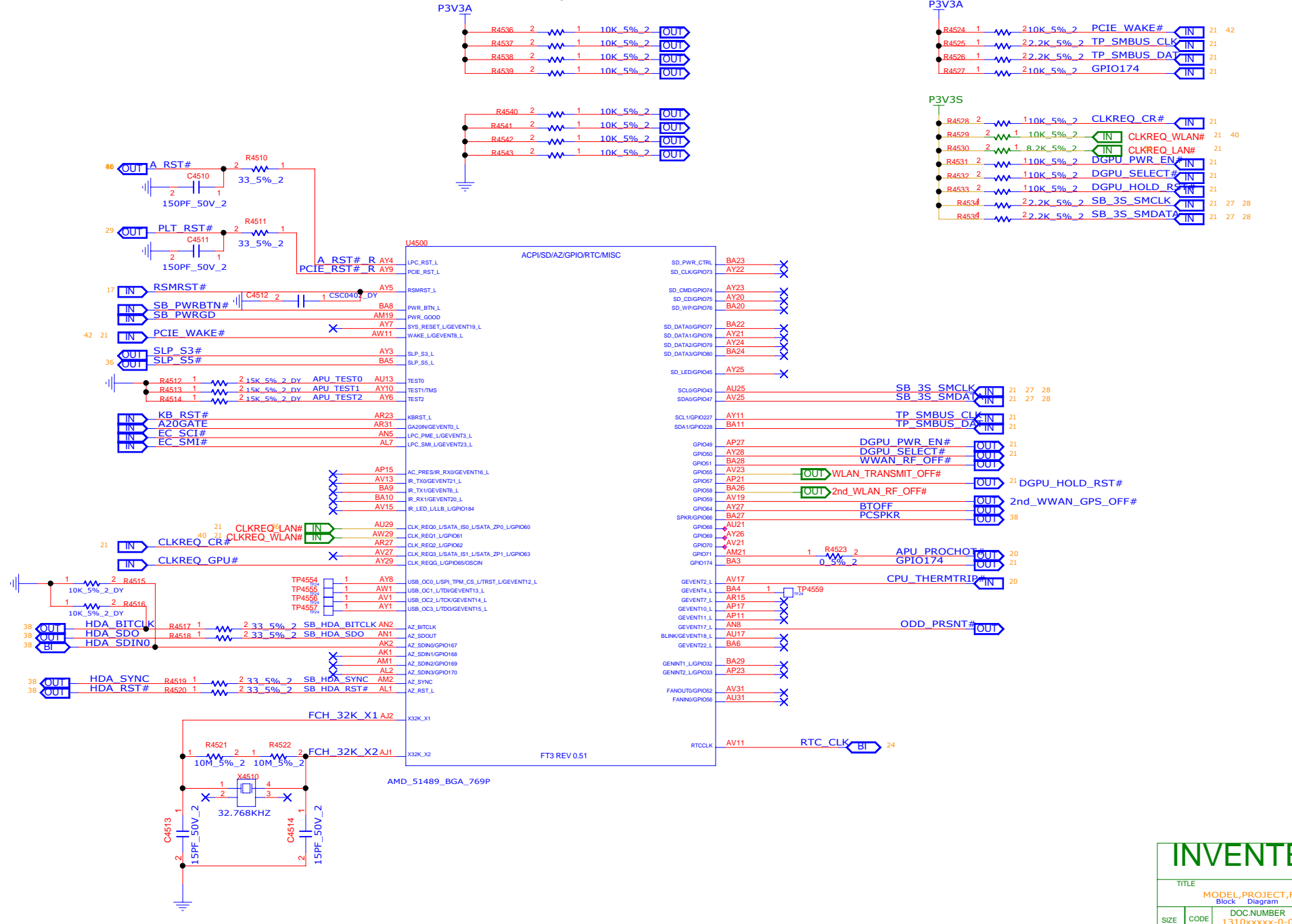
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8 7 6 5 4 3 2 1

D C B A

8 7 6 5 4 3 2 1

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D

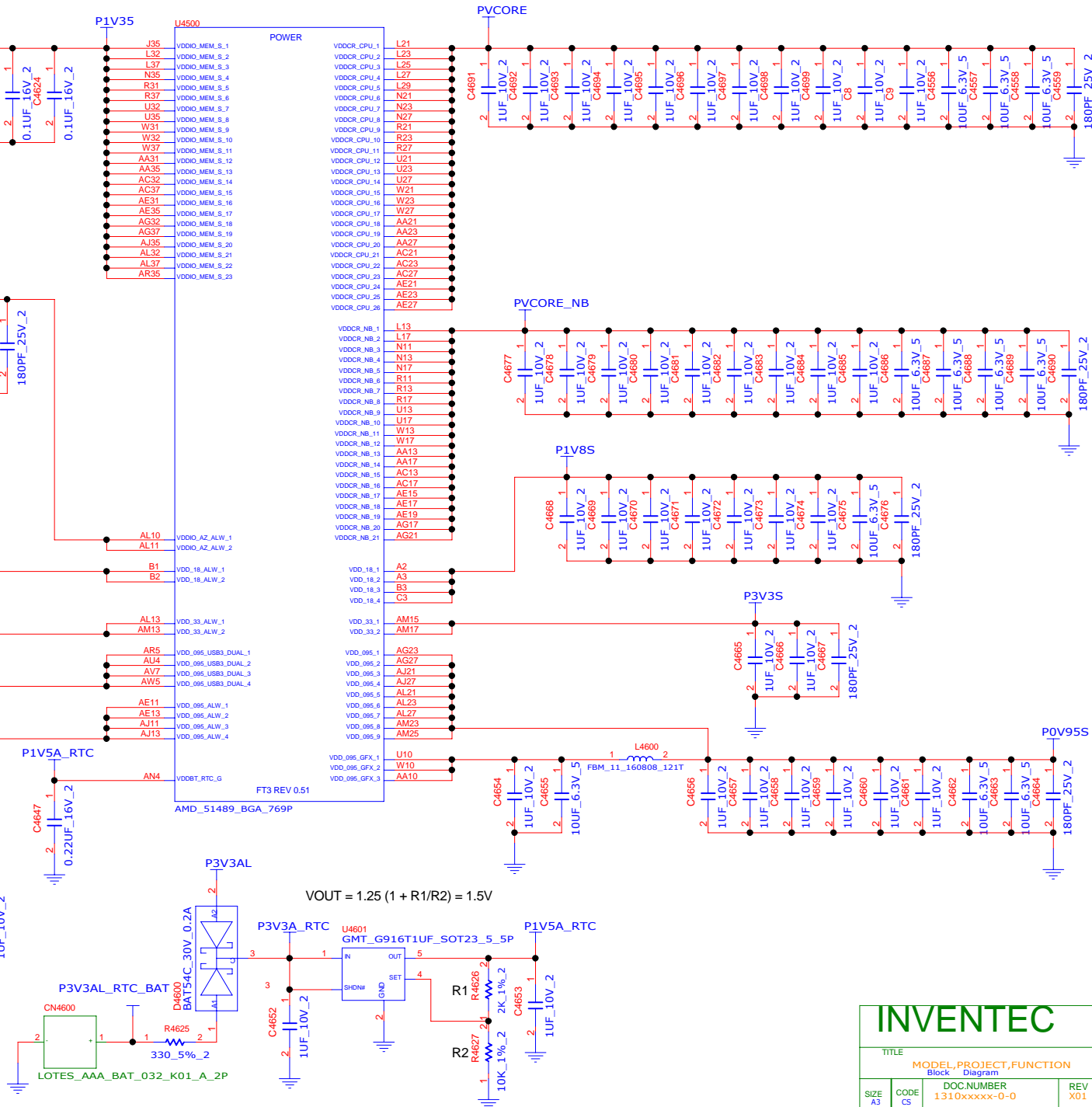
C

C

B

△

REFERENCE NUMBER 4600~4699



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
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D

C

B

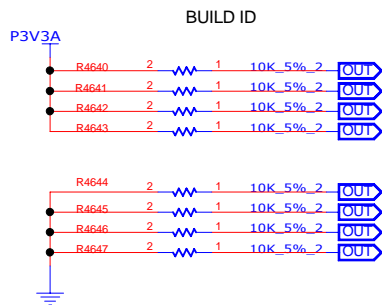
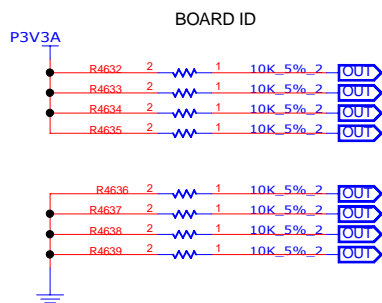
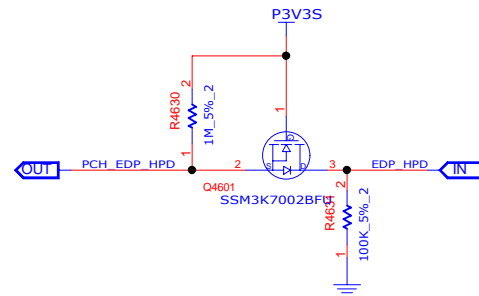
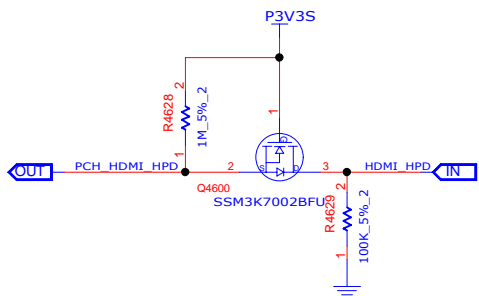
A

D

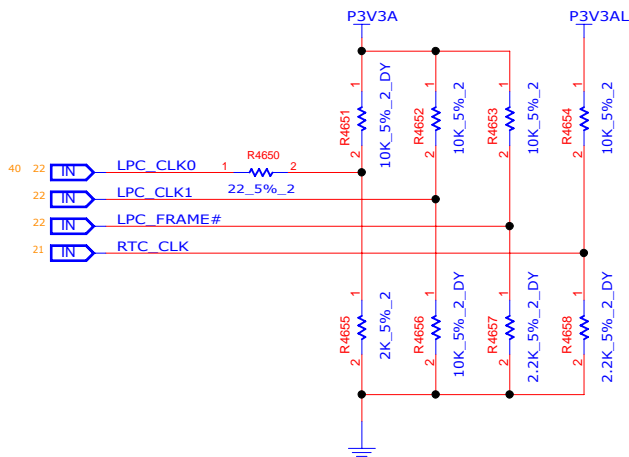
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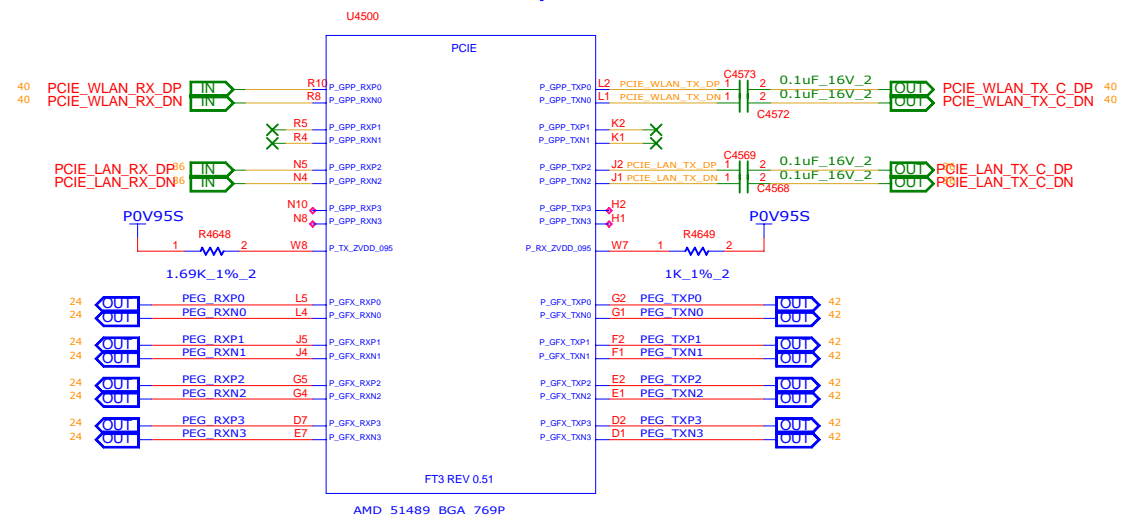
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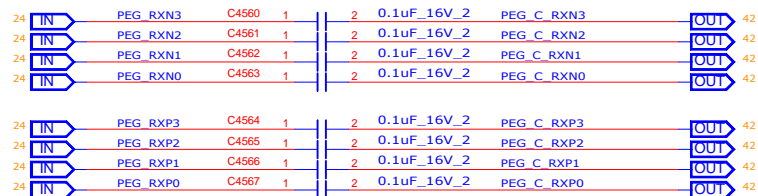
REFERENCE NUMBER 4600~4699



	LPC_CLK0	LPC_CLK1	LPC_FRAME#
PULL HIGH	BOOT FAIL TIMER ENABLED	CLKGEN ENABLED (DEFAULT)	SPI ROM (DEFAULT)
PULL LOW	BOOT FAIL TIMER DISABLED (DEFAULT)	CLKGEN DISABLED	LPC ROM

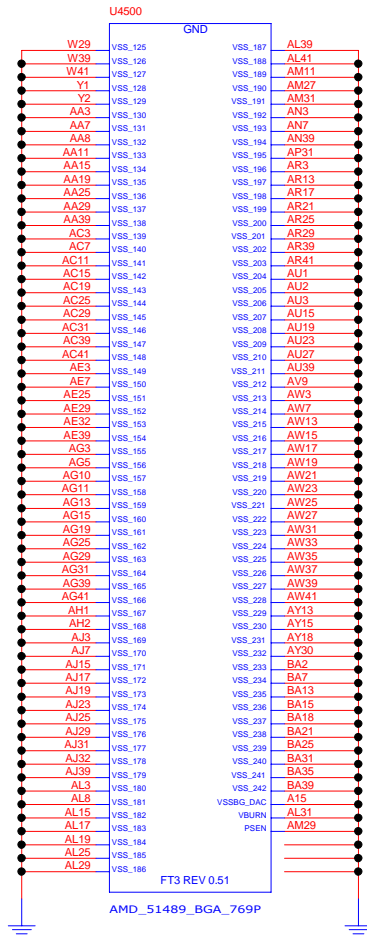
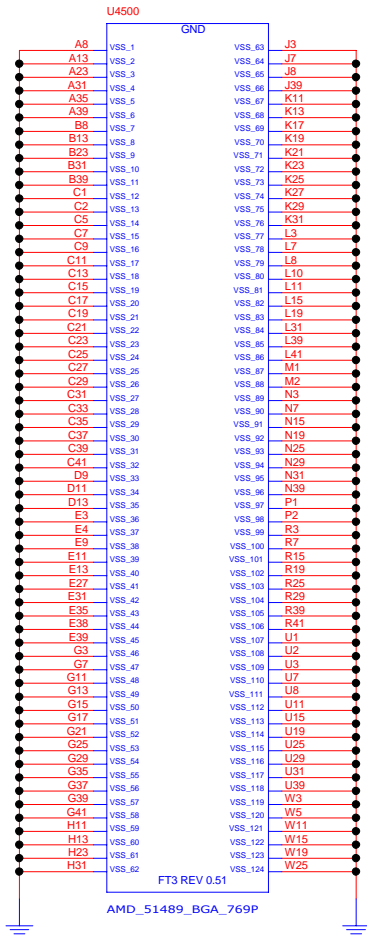


CLOSE TO SOC



INVENTEC

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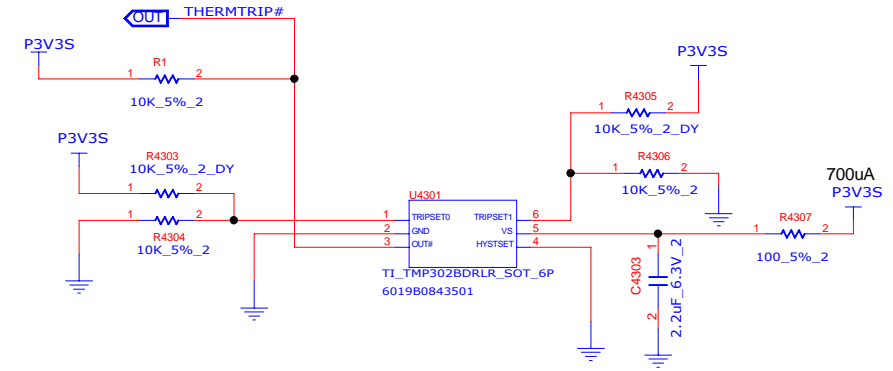
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

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REFERENCE NUMBER 4300~4349



DEVICE	SELECTABLE TRIP POINTS (°C)
TMP302A	50, 55, 60, 65
TMP302B	70, 75, 80, 85
TMP302C	90, 95, 100, 105
TMP302D	110, 115, 120, 125

Table 1. Trip Point vs $TRIP_{SET1}$ and $TRIP_{SET0}$

TRIP _{SET1}	TRIP _{SET0}	TMP302A	TMP302B	TMP302C	TMP302D
GND	GND	+50°C	+70°C	+90°C	+110°C
GND	V _S	+55°C	+75°C	+95°C	+115°C
V _S	GND	+60°C	+80°C	+100°C	+120°C
V _S	V _S	+65°C	+85°C	+105°C	+125°C

HYST _{SET}	THRESHOLD HYSTERESIS
GND	+5°C
V _S	+10°C

TITLE			
MODEL,PROJECT,FUNCTION THERMAL & FAN			
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8	7	6	5	4	3	2	1
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DDR H=9.2

INVENTEC

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MODEL,PROJECT,FUNCTION Block Diagram			
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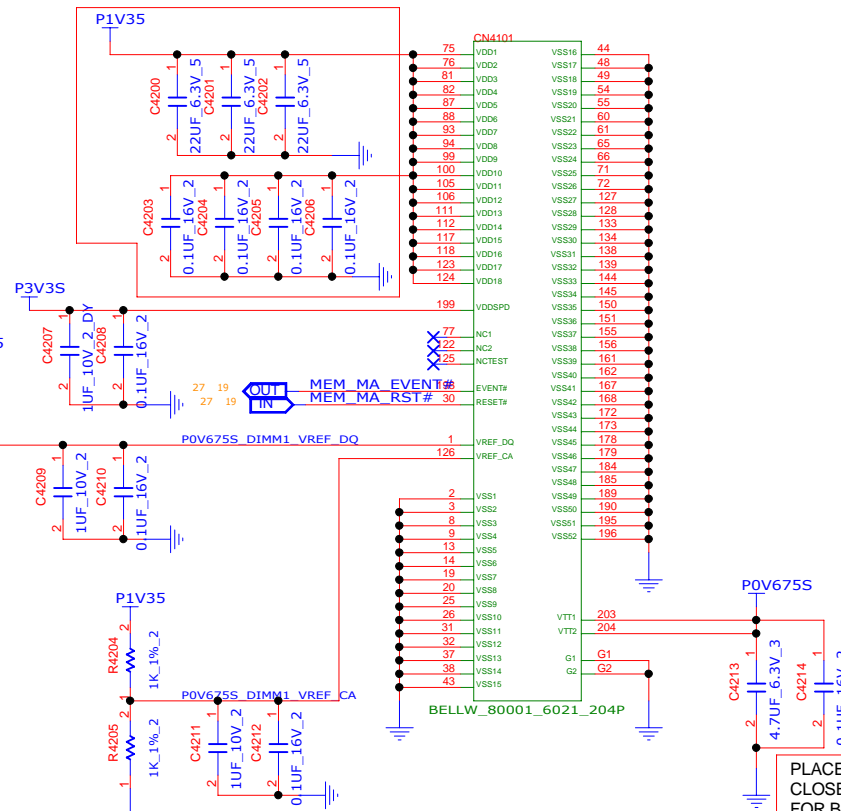
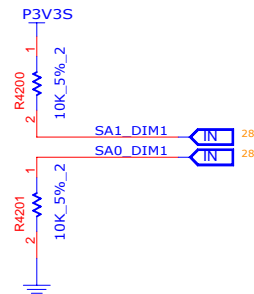
REFERENCE NUMBER 4200~4299

SO-DIMM0 POWER PIN
THESE CAPS NEAR
LAYOUT NOTE: PLACE

27	19	BI	MEM MA ADD(0)	98	CN4101	000	5	MEM MA DATA(0)	BI	19	27
27	19	BI	MEM MA ADD(1)	97	A0	001	7	MEM MA DATA(1)	BI	19	27
27	19	BI	MEM MA ADD(2)	96	A1	002	15	MEM MA DATA(2)	BI	19	27
27	19	BI	MEM MA ADD(3)	95	A2	003	17	MEM MA DATA(3)	BI	19	27
27	19	BI	MEM MA ADD(4)	92	A4	004	4	MEM MA DATA(4)	BI	19	27
27	19	BI	MEM MA ADD(5)	91	A5	005	6	MEM MA DATA(5)	BI	19	27
27	19	BI	MEM MA ADD(6)	90	A6	006	16	MEM MA DATA(6)	BI	19	27
27	19	BI	MEM MA ADD(7)	86	A7	007	18	MEM MA DATA(7)	BI	19	27
27	19	BI	MEM MA ADD(8)	89	A8	008	21	MEM MA DATA(8)	BI	19	27
27	19	BI	MEM MA ADD(9)	85	A9	009	23	MEM MA DATA(9)	BI	19	27
27	19	BI	MEM MA ADD(10)	107	A10_AP	010	33	MEM MA DATA(10)	BI	19	27
27	19	BI	MEM MA ADD(11)	84	A11	011	35	MEM MA DATA(11)	BI	19	27
27	19	BI	MEM MA ADD(12)	83	A12	012	22	MEM MA DATA(12)	BI	19	27
27	19	BI	MEM MA ADD(13)	119	A13	013	24	MEM MA DATA(13)	BI	19	27
27	19	BI	MEM MA ADD(14)	80	A14	014	34	MEM MA DATA(14)	BI	19	27
27	19	BI	MEM MA ADD(15)	78	A15	015	36	MEM MA DATA(15)	BI	19	27
27	19	BI	MEM MA BANK(0)	109	BA0	017	41	MEM MA DATA(17)	BI	19	27
27	19	BI	MEM MA BANK(1)	108	BA1	018	51	MEM MA DATA(18)	BI	19	27
27	19	BI	MEM MA BANK(2)	79	BA2	019	53	MEM MA DATA(19)	BI	19	27
27	19	IN	MEM MA1 CS#0	114	S0#	020	40	MEM MA DATA(20)	BI	19	27
19	19	IN	MEM MA1 CS#1	121	S1#	021	42	MEM MA DATA(21)	BI	19	27
19	19	IN	MEM MA CLK2 DP	101	CK2	022	50	MEM MA DATA(22)	BI	19	27
19	19	IN	MEM MA CLK2 DN	103	CK2#	023	52	MEM MA DATA(23)	BI	19	27
19	19	IN	MEM MA CLK3 DP	102	CK1	024	57	MEM MA DATA(24)	BI	19	27
19	19	IN	MEM MA CLK3 DN	104	CK1#	025	59	MEM MA DATA(25)	BI	19	27
19	19	IN	MEM MA1 CKE0	73	CKE0	026	67	MEM MA DATA(26)	BI	19	27
19	19	IN	MEM MA1 CKE1	74	CKE1	027	69	MEM MA DATA(27)	BI	19	27
27	19	IN	MEM MA CAS#	115	CAS#	028	56	MEM MA DATA(28)	BI	19	27
27	19	IN	MEM MA RAS#	110	RAS#	029	58	MEM MA DATA(29)	BI	19	27
27	19	IN	MEM MA WE#	113	WE#	030	68	MEM MA DATA(30)	BI	19	27
28	28	OUT	SA0 DIM1	197	SA0	031	70	MEM MA DATA(31)	BI	19	27
28	28	OUT	SA1 DIM1	201	SA1	032	129	MEM MA DATA(32)	BI	19	27
27	21	IN	SB 3S SMCLK	202	SCL	033	131	MEM MA DATA(33)	BI	19	27
27	21	IN	SB 3S SMDATA	200	SDA	034	141	MEM MA DATA(34)	BI	19	27
19	19	IN	MEM MA1 ODT0	116	ODT0	036	143	MEM MA DATA(36)	BI	19	27
19	19	IN	MEM MA1 ODT1	120	ODT1	037	132	MEM MA DATA(37)	BI	19	27
27	19	IN	MEM MA DM(0)	11	DM0	038	140	MEM MA DATA(38)	BI	19	27
27	19	IN	MEM MA DM(1)	28	DM1	039	142	MEM MA DATA(39)	BI	19	27
27	19	IN	MEM MA DM(2)	46	DM2	040	147	MEM MA DATA(40)	BI	19	27
27	19	IN	MEM MA DM(3)	63	DM3	041	149	MEM MA DATA(41)	BI	19	27
27	19	IN	MEM MA DM(4)	136	DM4	043	157	MEM MA DATA(42)	BI	19	27
27	19	IN	MEM MA DM(5)	153	DM5	044	159	MEM MA DATA(43)	BI	19	27
27	19	IN	MEM MA DM(6)	170	DM6	045	146	MEM MA DATA(44)	BI	19	27
27	19	IN	MEM MA DM(7)	187	DM7	046	148	MEM MA DATA(45)	BI	19	27
27	19	IN	MEM MA DM(7)	187	DM7	046	158	MEM MA DATA(46)	BI	19	27
27	19	BI	MEM MA DQS0 DP	12	DQS0	047	160	MEM MA DATA(47)	BI	19	27
27	19	BI	MEM MA DQS1 DP	29	DQS1	048	163	MEM MA DATA(48)	BI	19	27
27	19	BI	MEM MA DQS2 DP	47	DQS2	049	165	MEM MA DATA(49)	BI	19	27
27	19	BI	MEM MA DQS3 DP	64	DQS3	050	175	MEM MA DATA(50)	BI	19	27
27	19	BI	MEM MA DQS4 DP	137	DQS4	051	177	MEM MA DATA(51)	BI	19	27
27	19	BI	MEM MA DQS5 DP	154	DQS5	052	164	MEM MA DATA(52)	BI	19	27
27	19	BI	MEM MA DQS6 DP	171	DQS6	053	166	MEM MA DATA(53)	BI	19	27
27	19	BI	MEM MA DQS7 DP	188	DQS7	054	174	MEM MA DATA(54)	BI	19	27
27	19	BI	MEM MA DQS0 DN	10	DQS0	055	176	MEM MA DATA(55)	BI	19	27
27	19	BI	MEM MA DQS1 DN	27	DQS1	056	181	MEM MA DATA(56)	BI	19	27
27	19	BI	MEM MA DQS2 DN	45	DQS2	057	183	MEM MA DATA(57)	BI	19	27
27	19	BI	MEM MA DQS3 DN	62	DQS3	058	191	MEM MA DATA(58)	BI	19	27
27	19	BI	MEM MA DQS4 DN	135	DQS4	059	193	MEM MA DATA(59)	BI	19	27
27	19	BI	MEM MA DQS5 DN	152	DQS5	060	180	MEM MA DATA(60)	BI	19	27
27	19	BI	MEM MA DQS6 DN	169	DQS6	061	182	MEM MA DATA(61)	BI	19	27
27	19	BI	MEM MA DQS7 DN	186	DQS7	062	192	MEM MA DATA(62)	BI	19	27
27	19	BI	MEM MA DQS7 DN	186	DQS7	063	194	MEM MA DATA(63)	BI	19	27

BELLW_80001_6021_204P

Note :
SO-DIMMA SPD Address is 0xA4
SO-DIMMA TS Address is 0x34

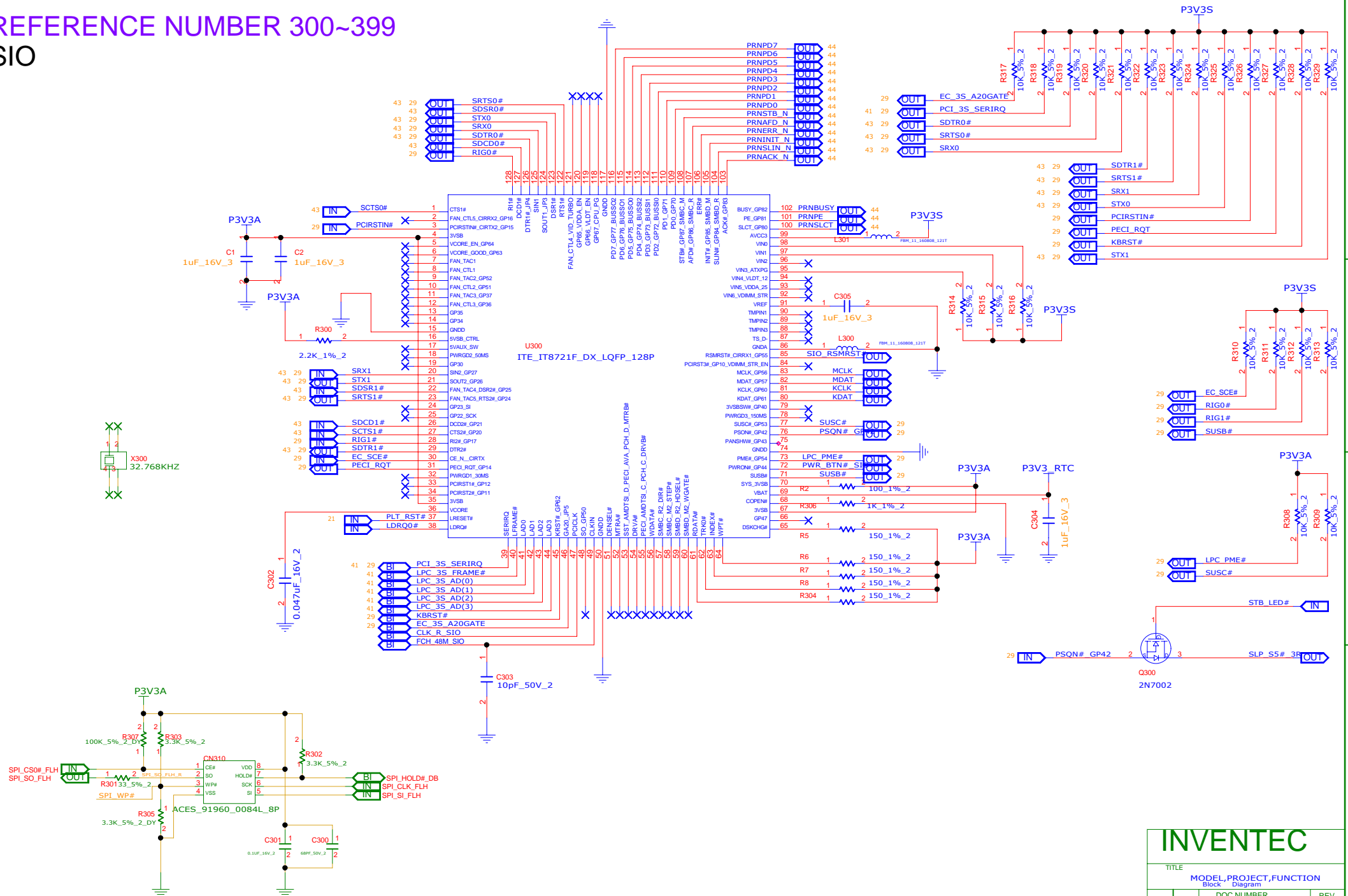


FOR EMI PURPOSE, PLACE ACROSS SPLIT PLANE

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
DOC NUMBER			
1310xxxxx-0-0			
SIZE	CODE	REV	
A3	CS	X01	

REFERENCE NUMBER 300~399
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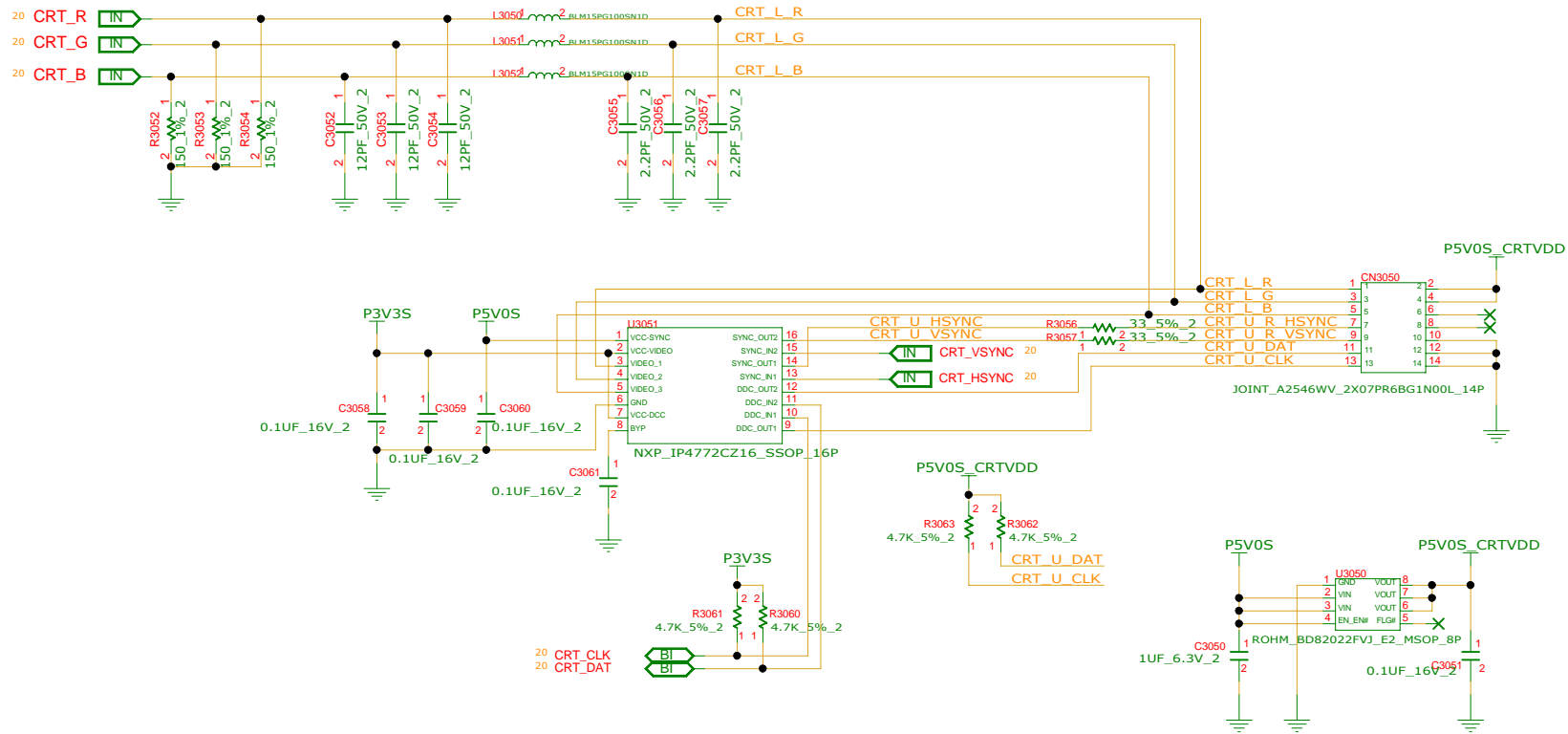


INVENTEC

TITLE	MODEL PROJECT FUNCTION
1. Project Overview	1. Project Overview
2. Project Objectives	2. Project Objectives
3. Project Scope	3. Project Scope
4. Project Organization	4. Project Organization
5. Project Schedule	5. Project Schedule
6. Project Budget	6. Project Budget
7. Project Risks	7. Project Risks
8. Project Communication	8. Project Communication
9. Project Monitoring and Control	9. Project Monitoring and Control
10. Project Closure	10. Project Closure

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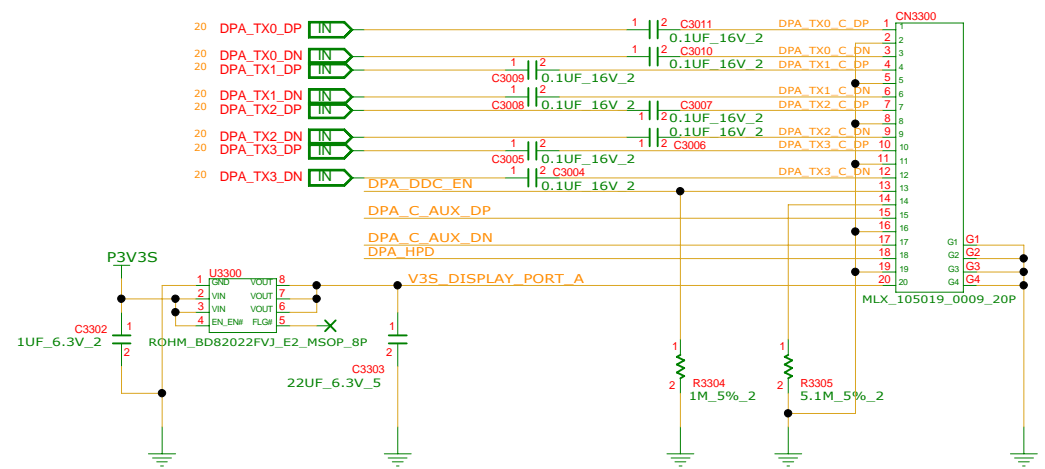
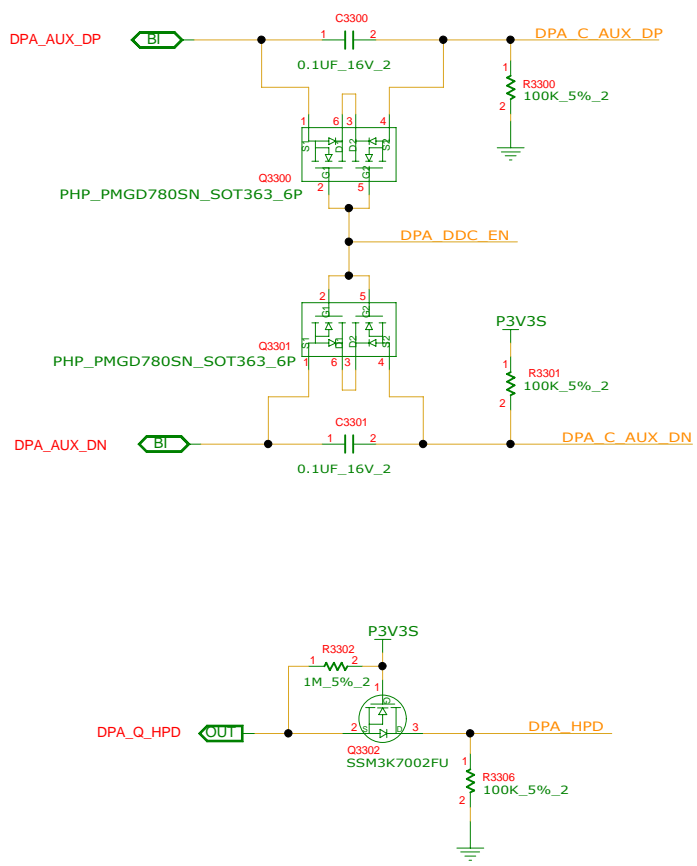
CRT

REFERENCE NUMBER 3050~3099

INVENTEC			
TITLE			
MODEL PROJECT,FUNCTION			
CRT CONN			
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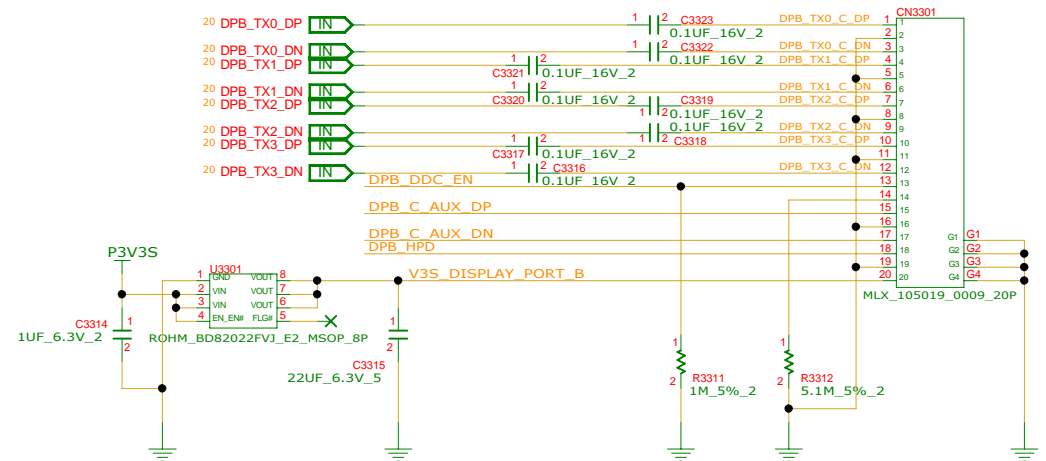
DISPLAY PORT A



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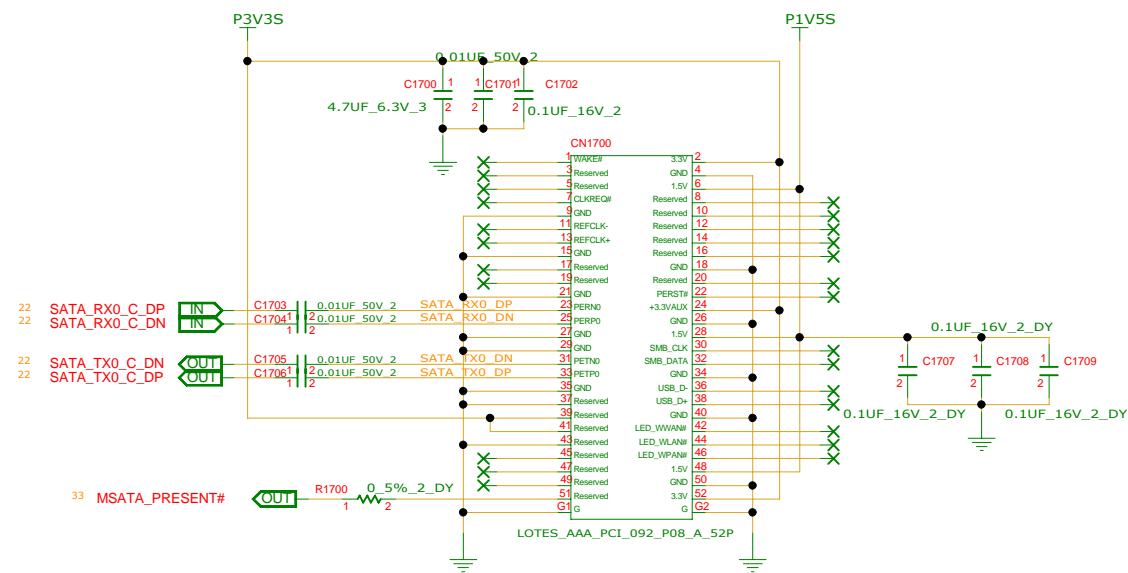
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A

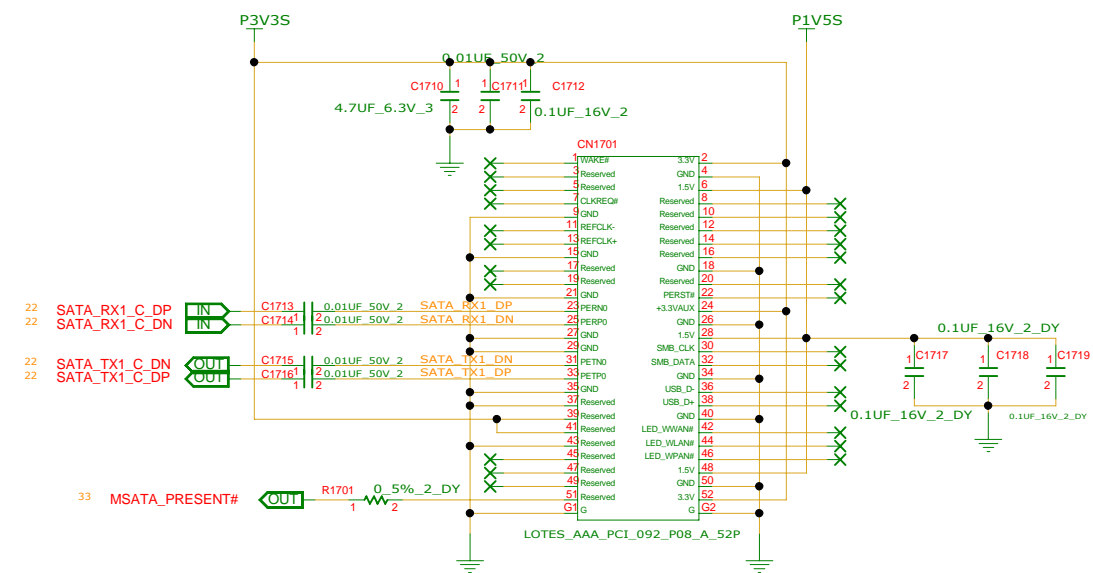


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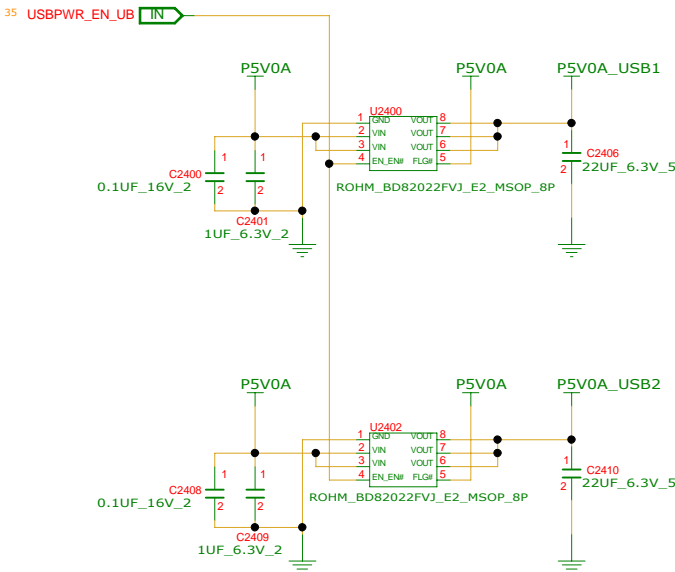
PRIMARY STORAGE



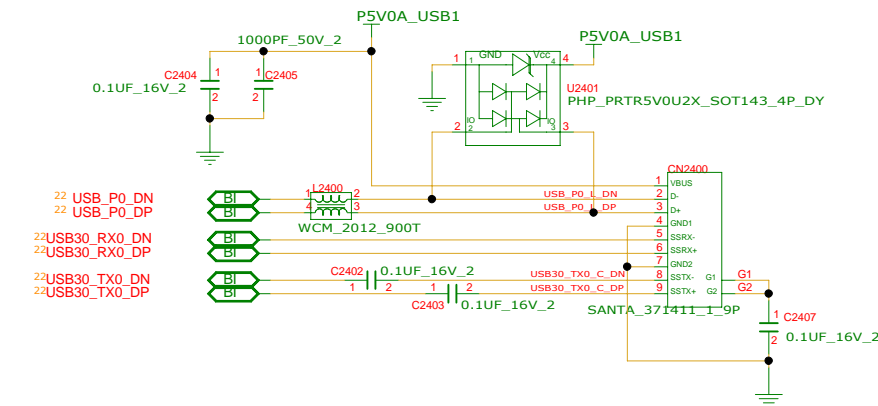
SECONDARY STORAGE

INVENTEC			
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MODEL, PROJECT, FUNCTION			
SATA HDD & SATA ODD			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
CHANGE by XXX DATE 21-OCT-2002 SHEET 33 of 46			

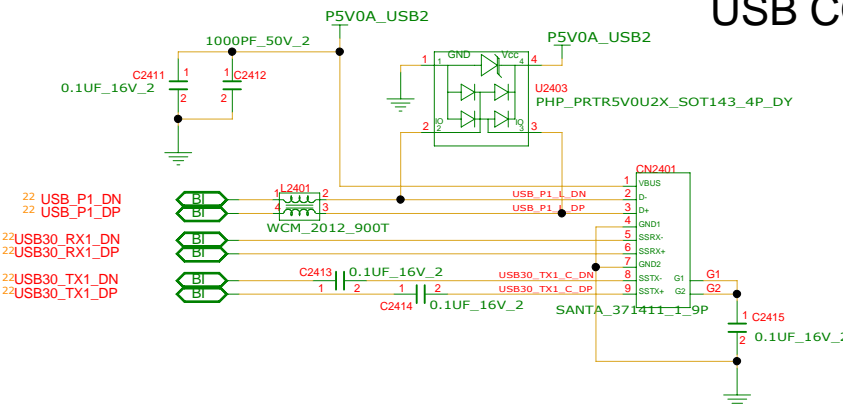
USB3.0



USB CONN01



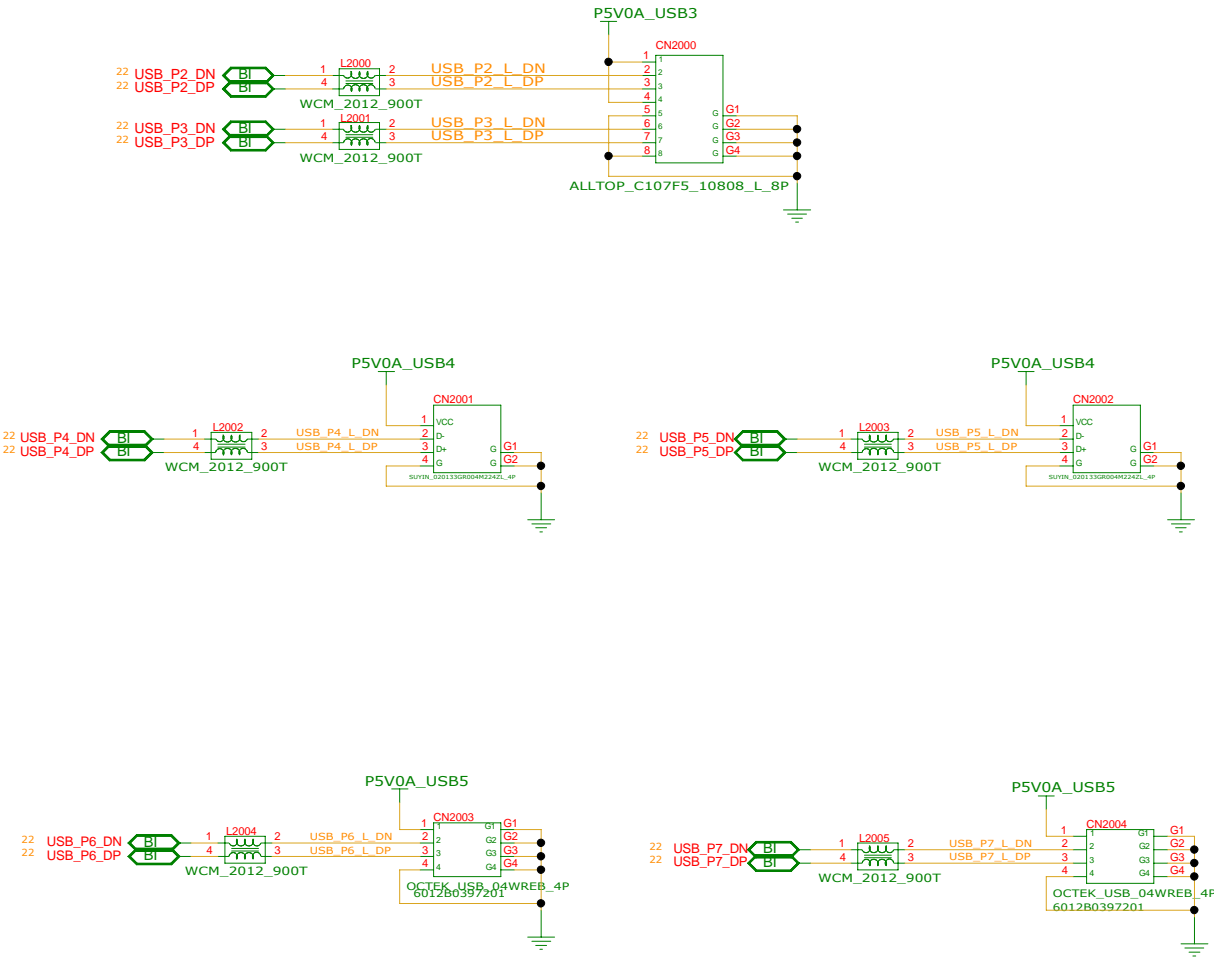
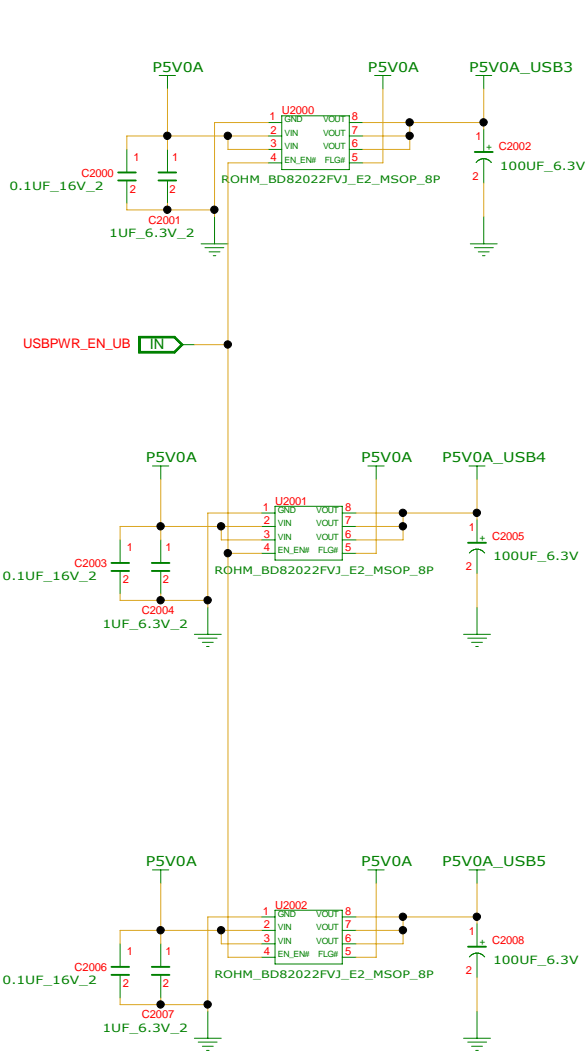
USB CONN02



REFERENCE NUMBER 2400~2499

INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
CHANGE by		DATE	SHEET
XXX		21-OCT-2002	34 of 46

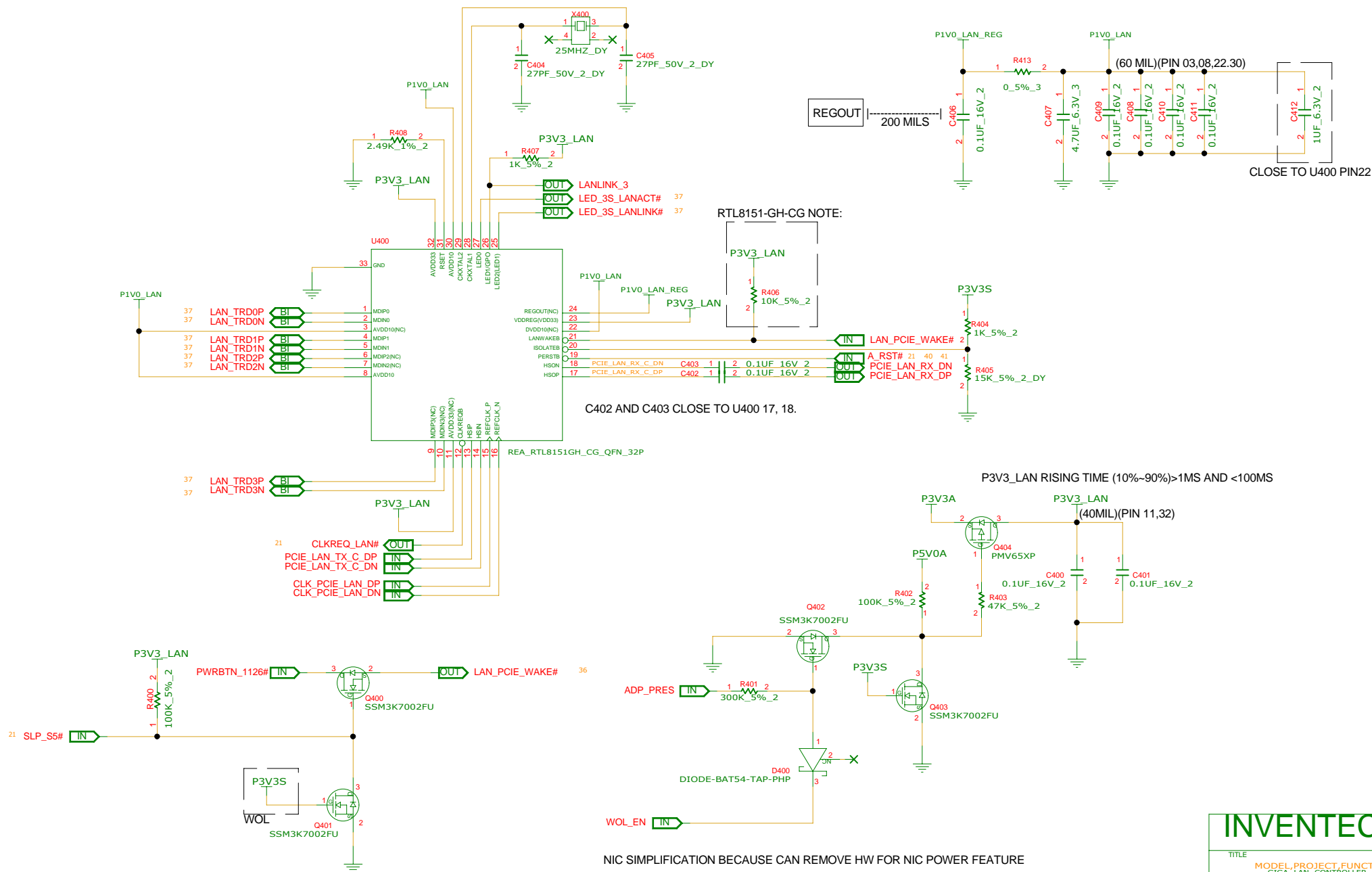
USB 2.0 CONN



REFERENCE NUMBER 2000~2149

INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
USB, POWER BUTTON DB			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 35 of 46			

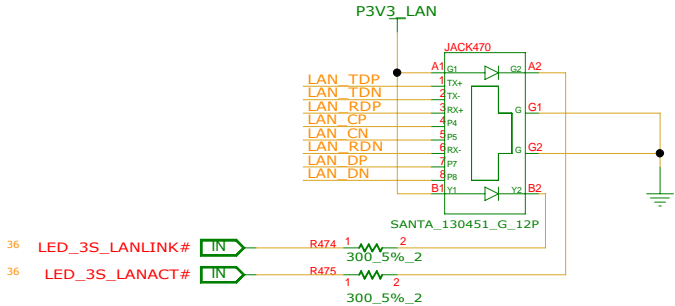
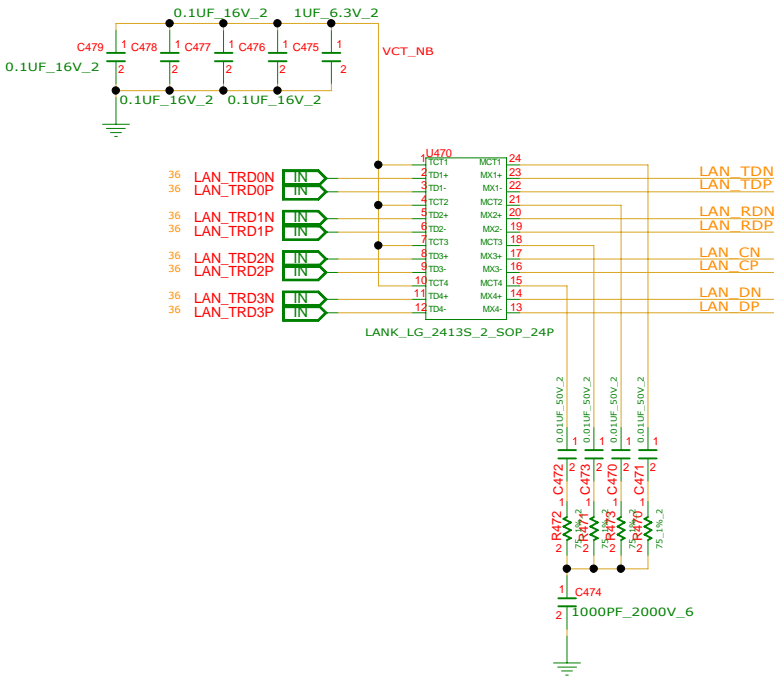
REFERENCE NUMBER 400~469



INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
GIGA LAN CONTROLLER			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 36 of 46			

CHANGE by XXX DATE 21-OCT-2002

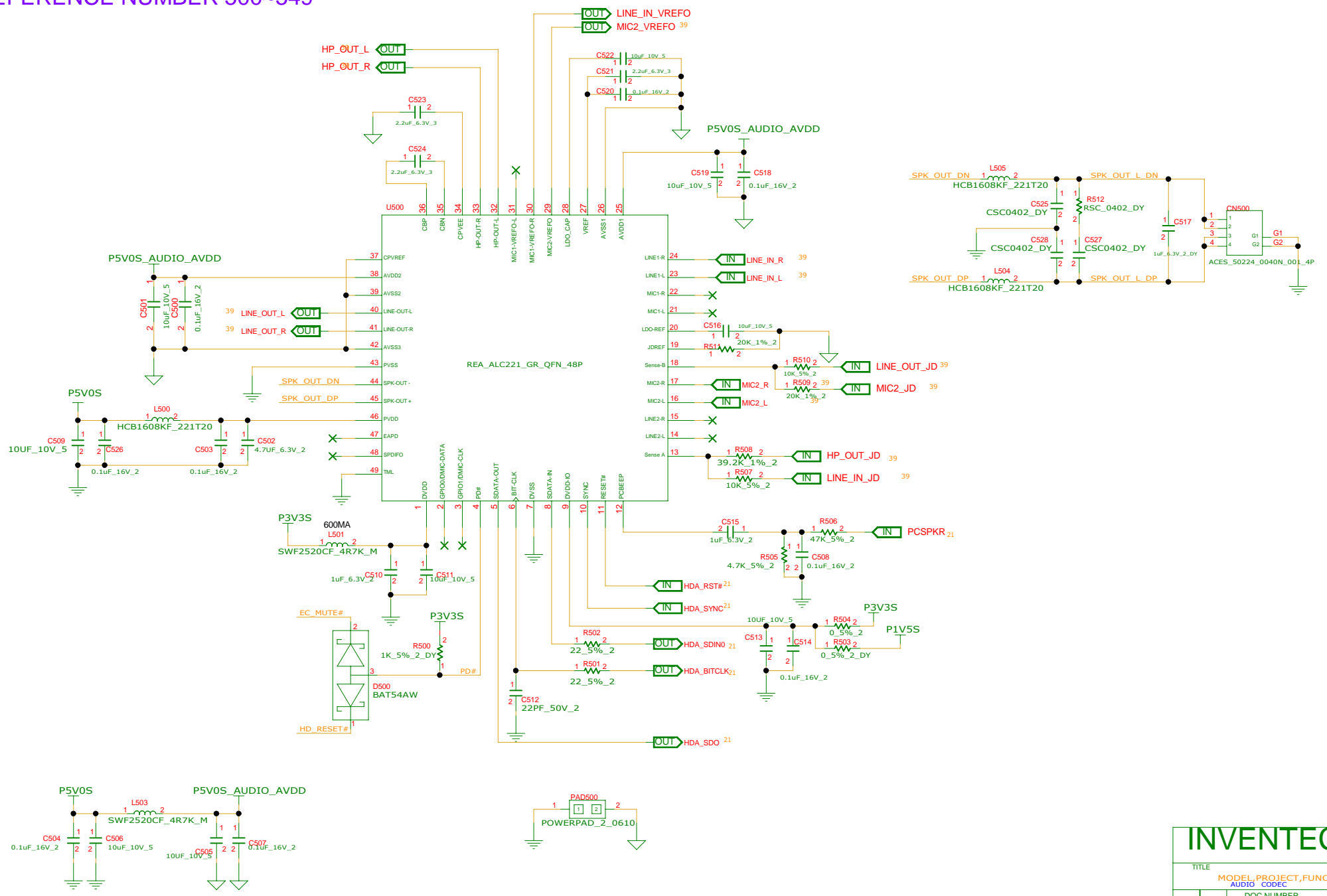
REFERENCE NUMBER 470~499



INVENTEC

TITLE			
MODEL PROJECT FUNCTION TRANSFORMER & RJ45_CONN			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01

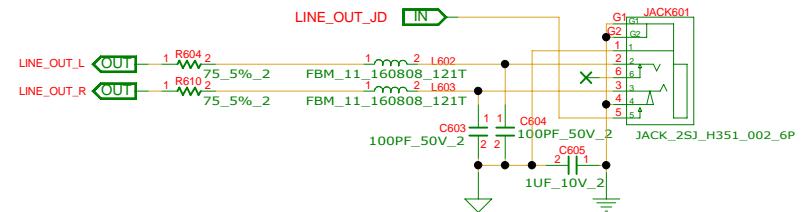
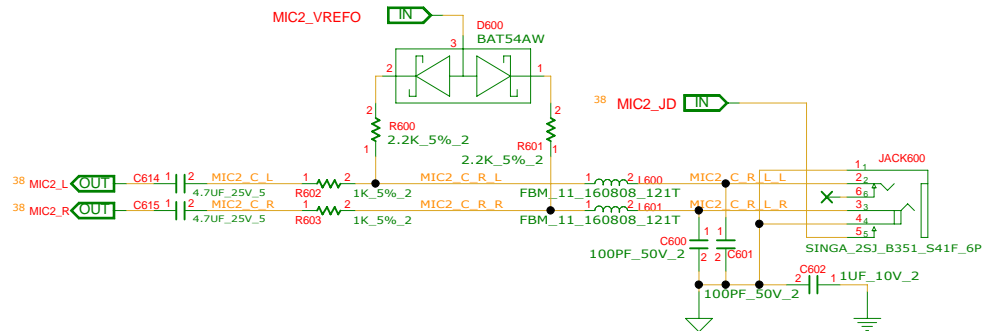
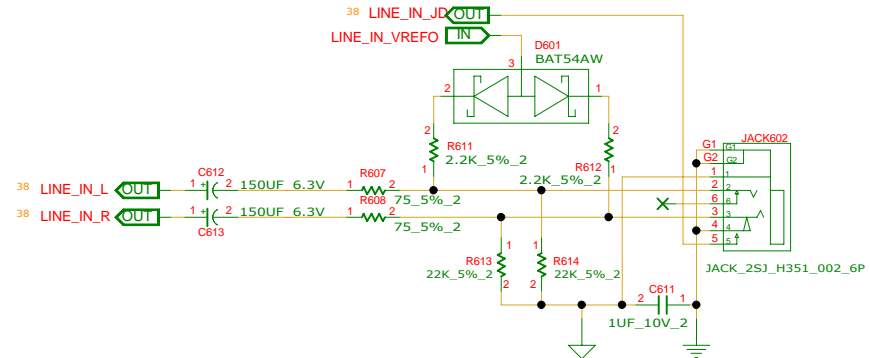
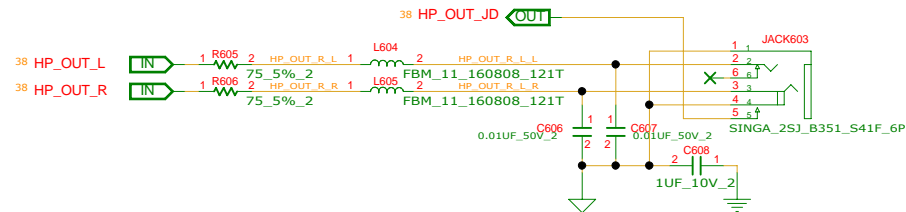
REFERENCE NUMBER 500~549



INVENTEC

TITLE			
MODEL PROJECT,FUNCTION			
AUDIO CODEC			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

REFERENCE NUMBER 600~610



INVENTEC

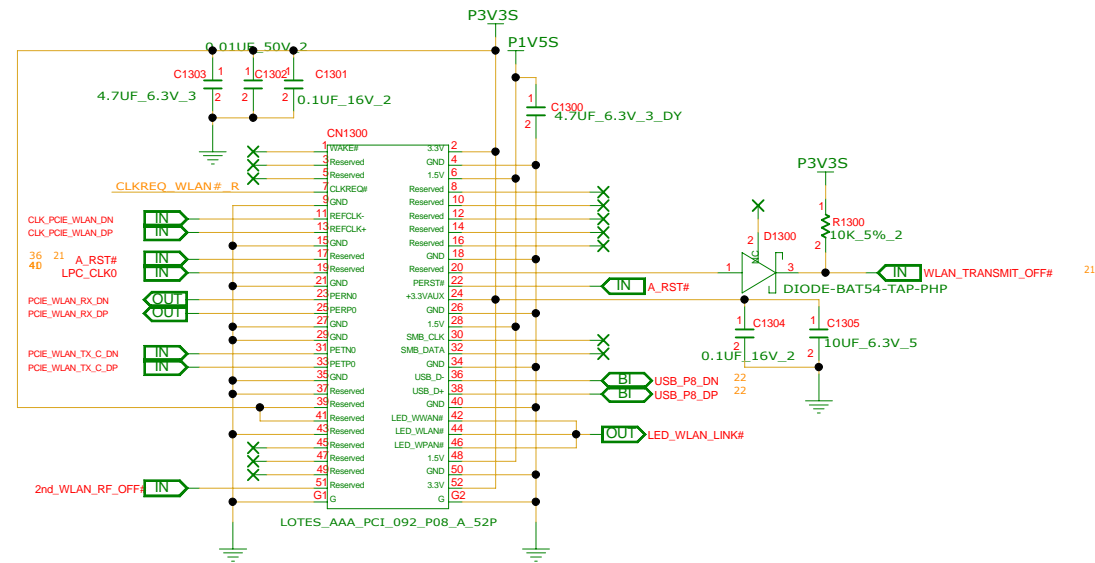
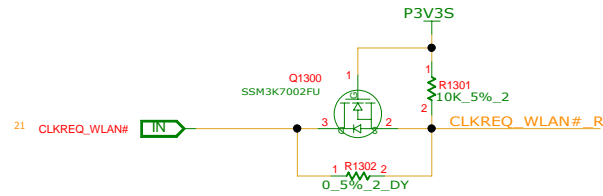
TITLE
MODEL, PROJECT, FUNCTION
HP JACK & MIC JACK

SIZE CODE DOC NUMBER REV
A3 CS 1310xxxxx-0-0 X01

CHANGE by XXX DATE 21-OCT-2002

SHEET 39 of 46

WLAN



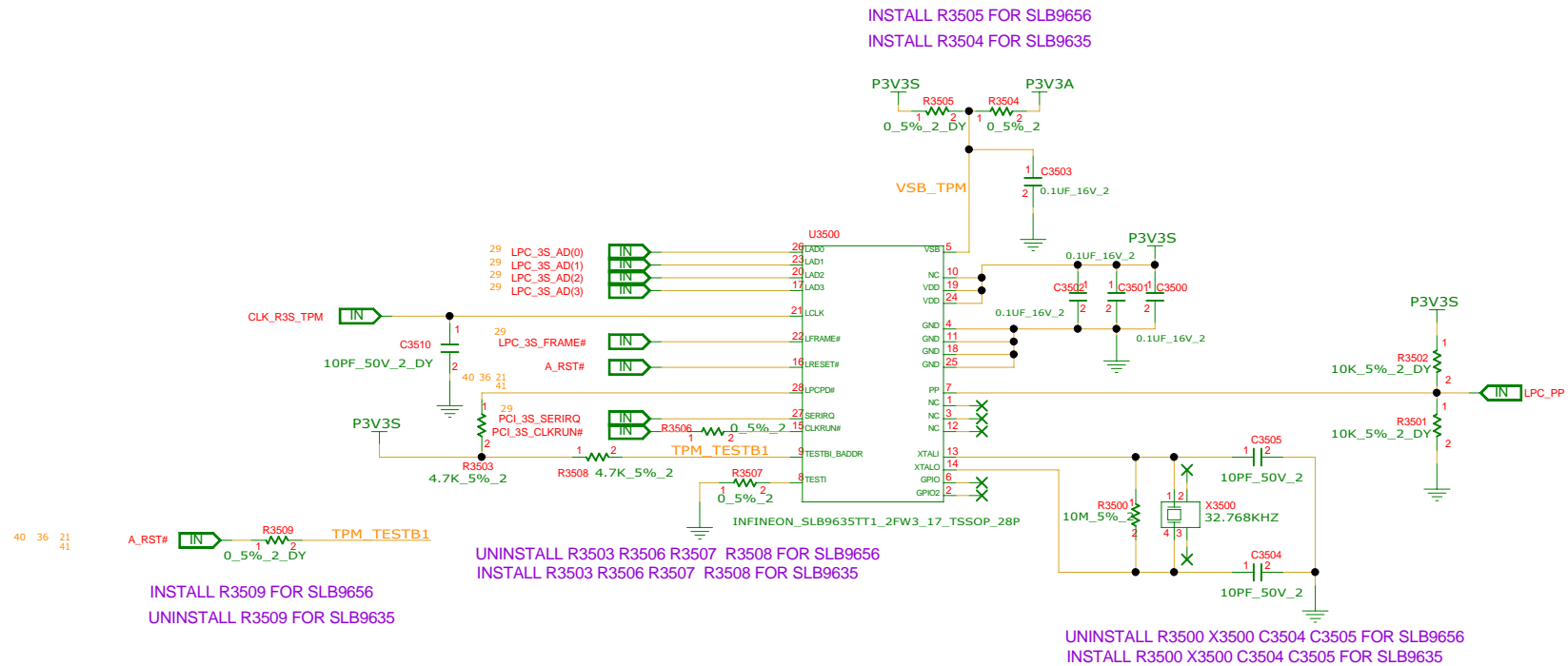
INVENTEC

TITLE	MODEL,PROJECT,FUNCTION
WLAN, S. PT	

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
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SHEET 40 of 46

REFERENCE NUMBER 3500~3599



INVENTEC

TITLE MODEL,PROJECT,FUNCTION

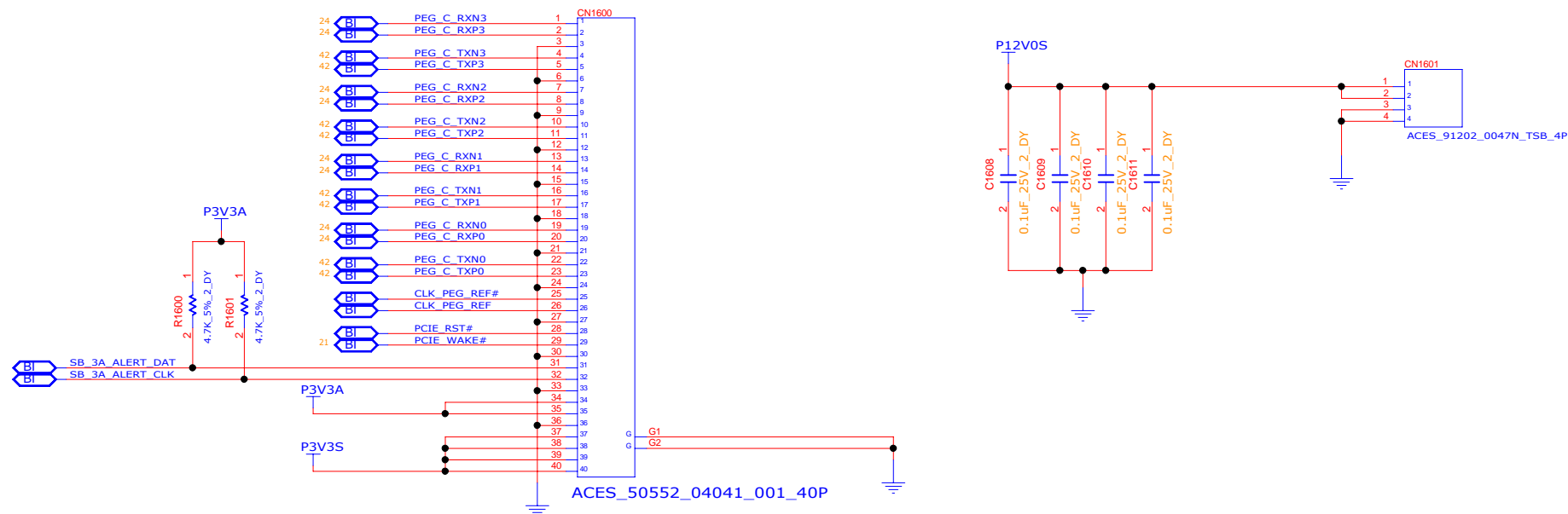
SIZE CODE DOC NUMBER REV

A3 CS 1310xxxx-0-0 X01

CHANGE by XXX DATE 21-OCT-2002

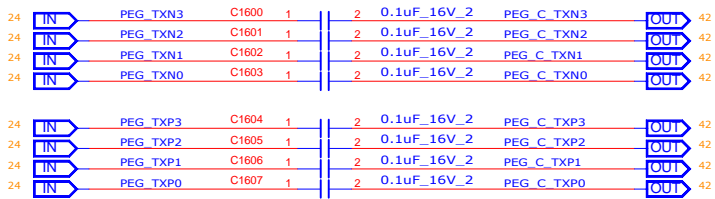
SHEET 41 of 46

RISER CARD CONN



6012B0178901

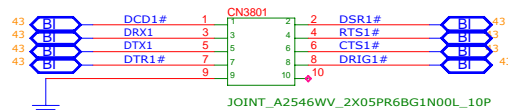
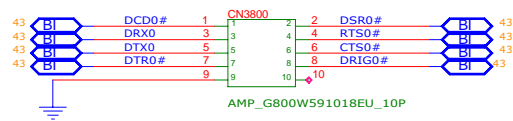
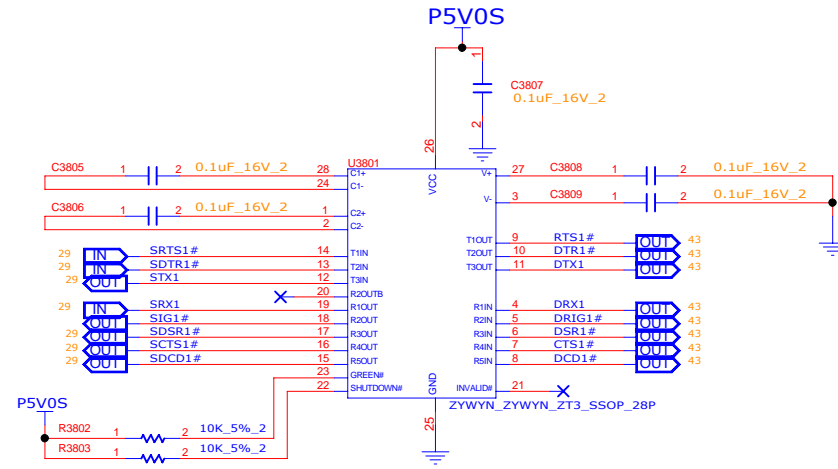
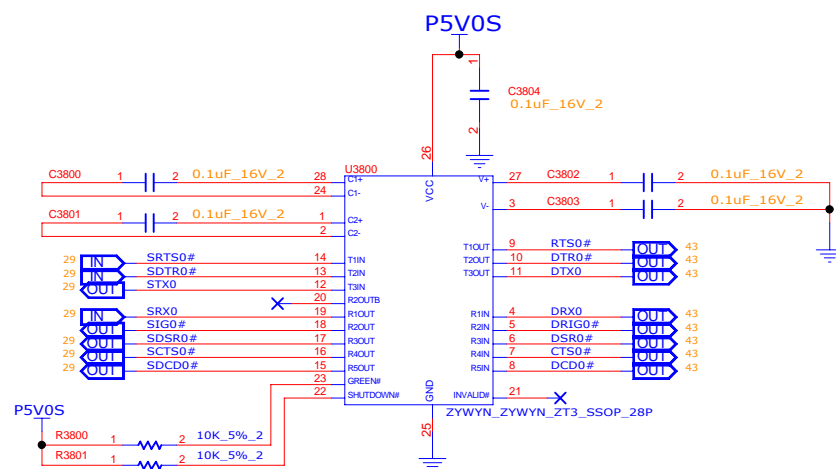
CLOSE TO CONN



REFERENCE NUMBER 1600~1699

INVENTEC			
TITLE MODEL,PROJECT,FUNCTION			
GPU-1			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
CHANGE by XXX		DATE 21-OCT-2002	SHEET 42 of 46

Serial Conn



FLEX OPTION

REFERENCE NUMBER 3800~3899

INVENTEC

TITLE MODEL PROJECT,FUNCTION

GPU-3

DOC NUMBER 1310xxxxx-0-0

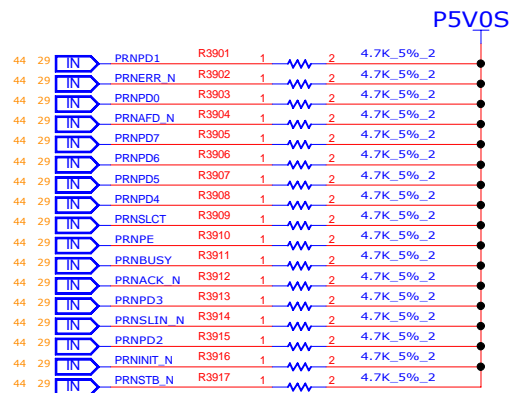
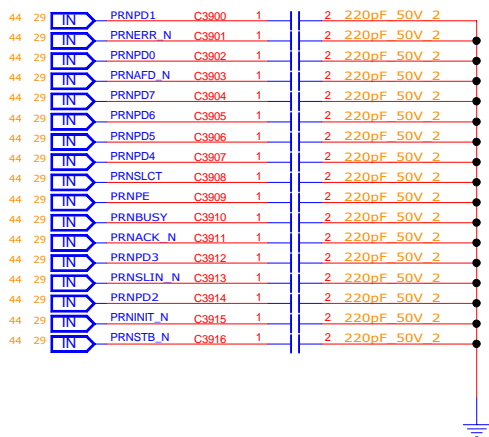
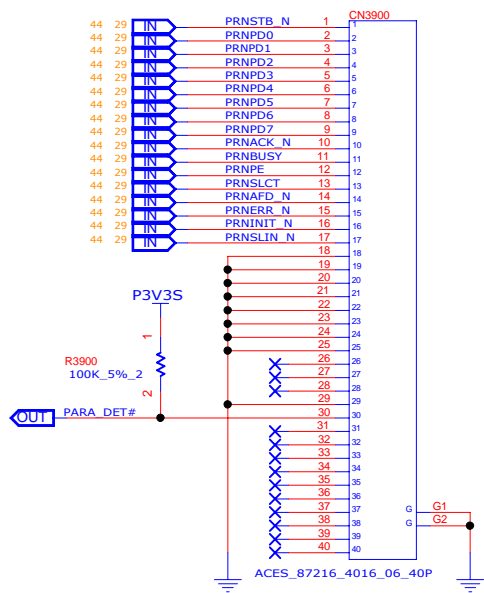
REV X01

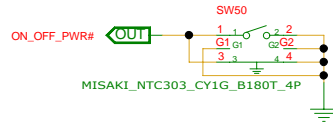
CHANGE by XXX DATE 21-OCT-2002

SHEET 43 of 46

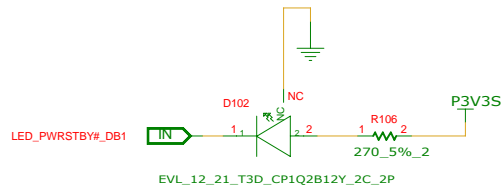
REFERENCE NUMBER 3900~3999

Parallell port Header



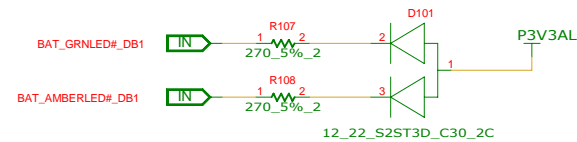


POWER SWITCH



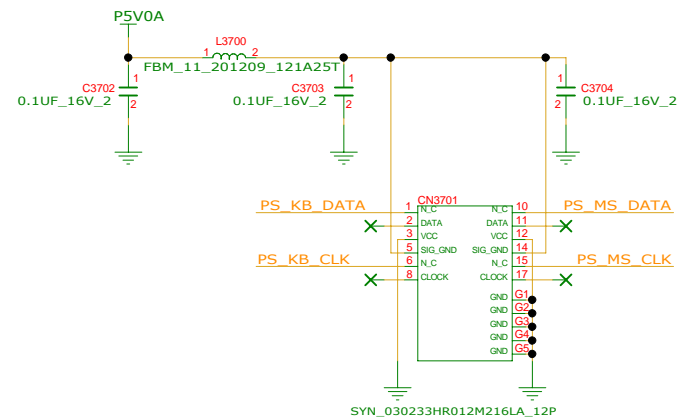
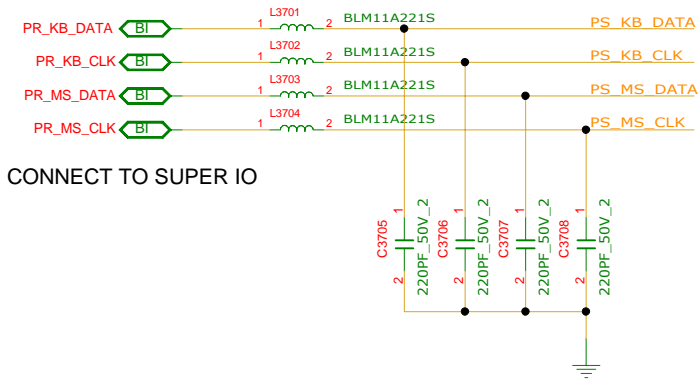
D102 NEEDED TO BE REPLACED BY 6011B0022801

POWER LED



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01



INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
PS2 SERIAL PARALLEL PORT			
DOC NUMBER			
1310xxxxx-0-0			
REV			
X01			